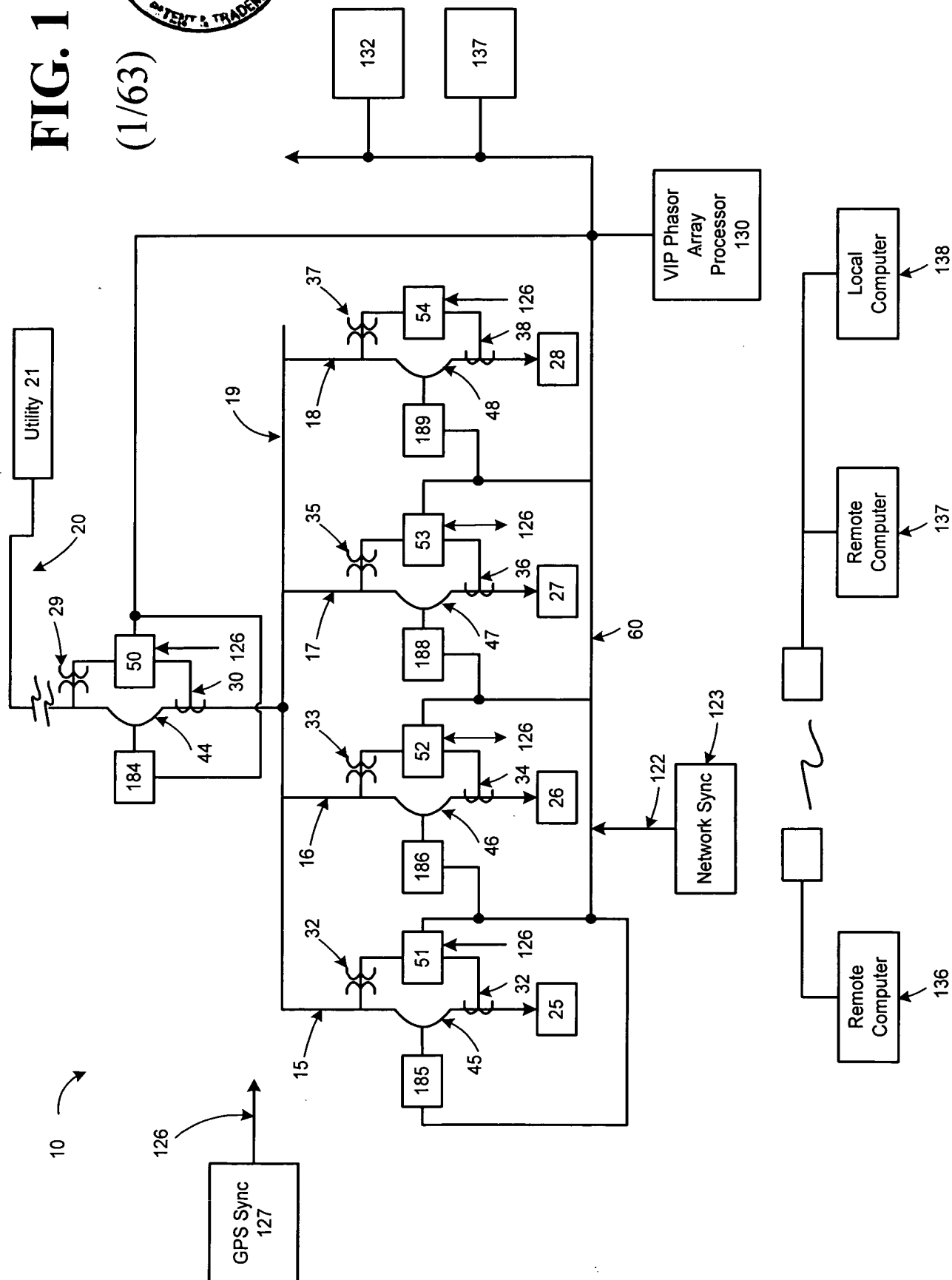




FIG. 1

(1/63)



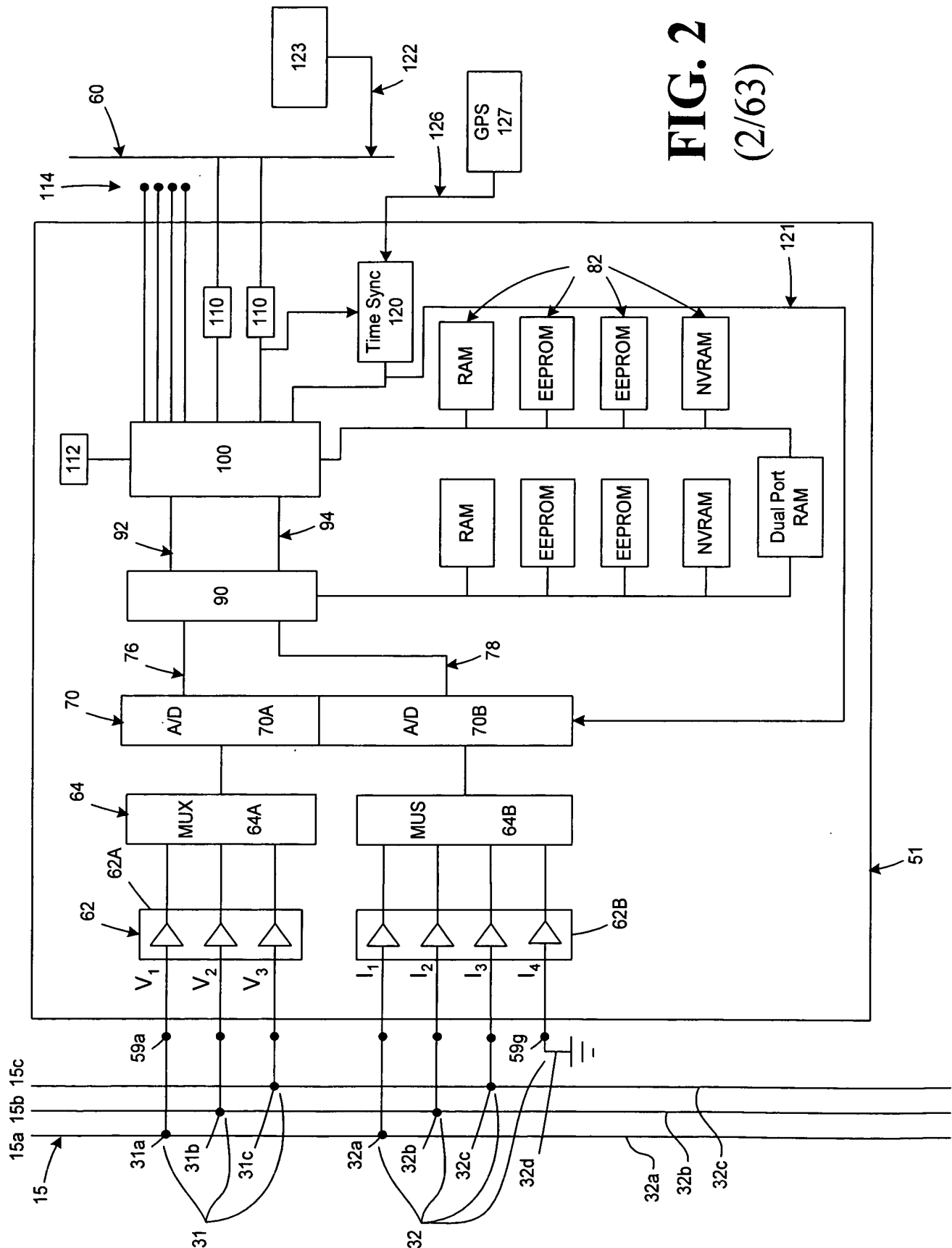
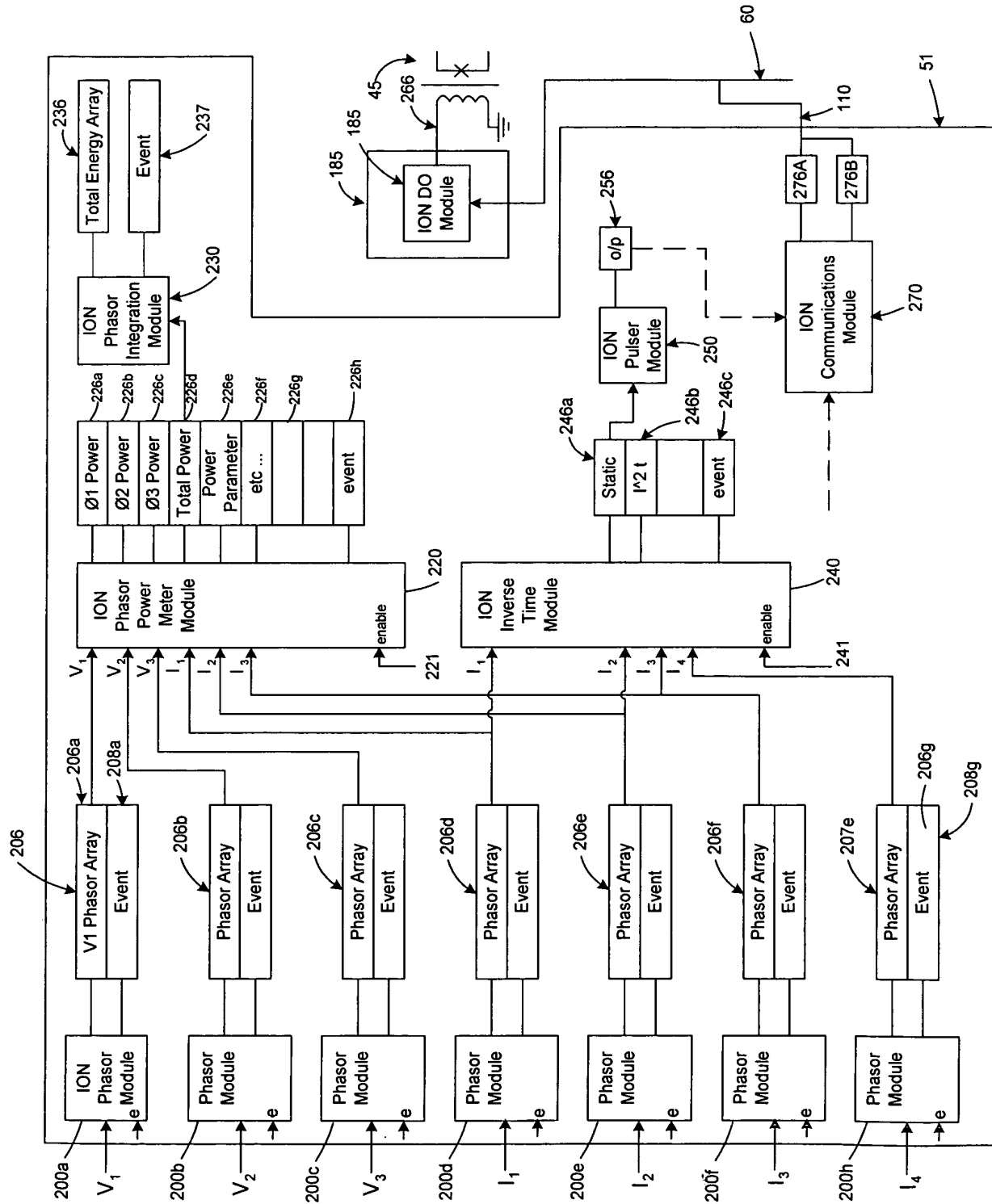
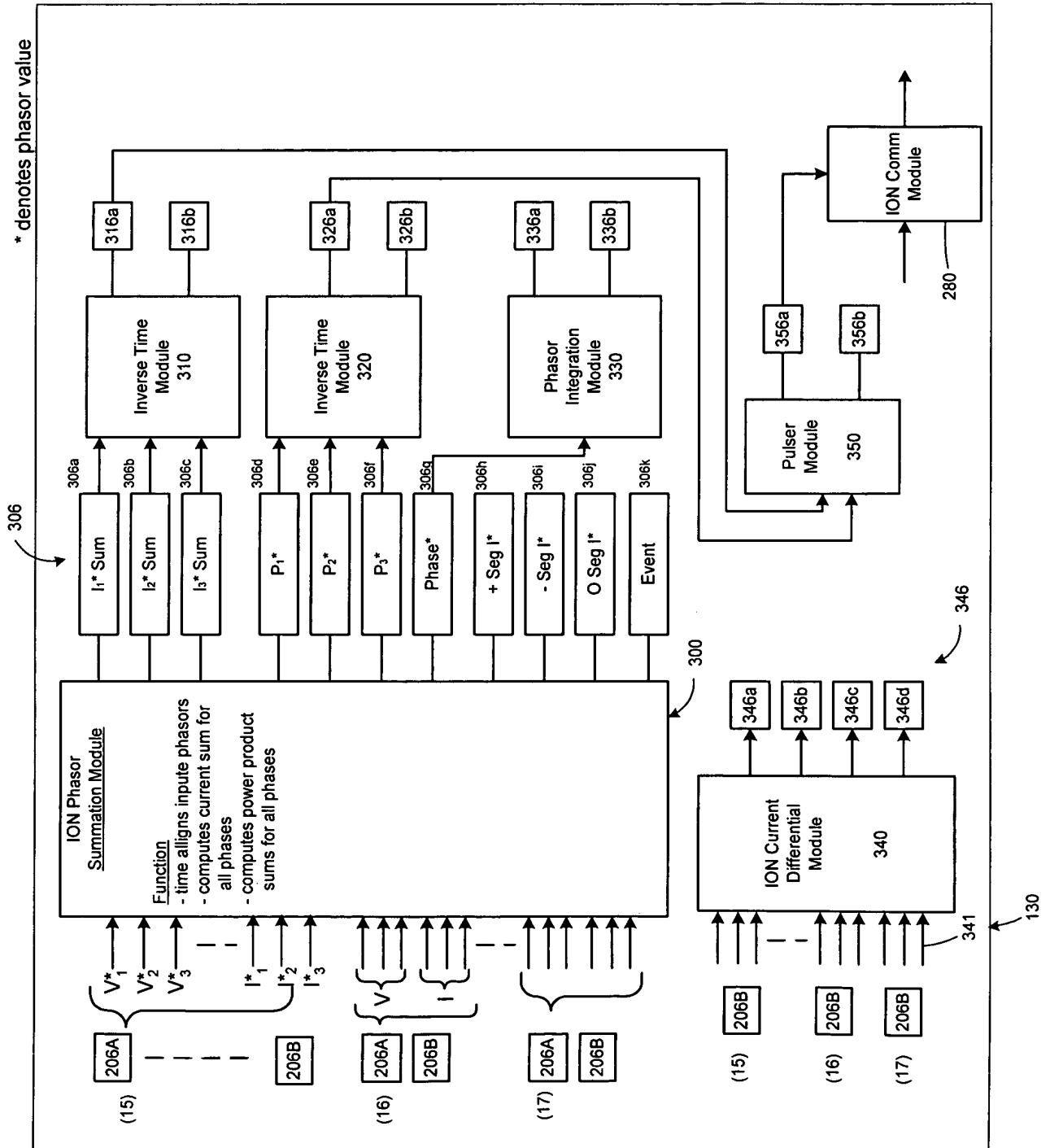


FIG. 2
(2/63)

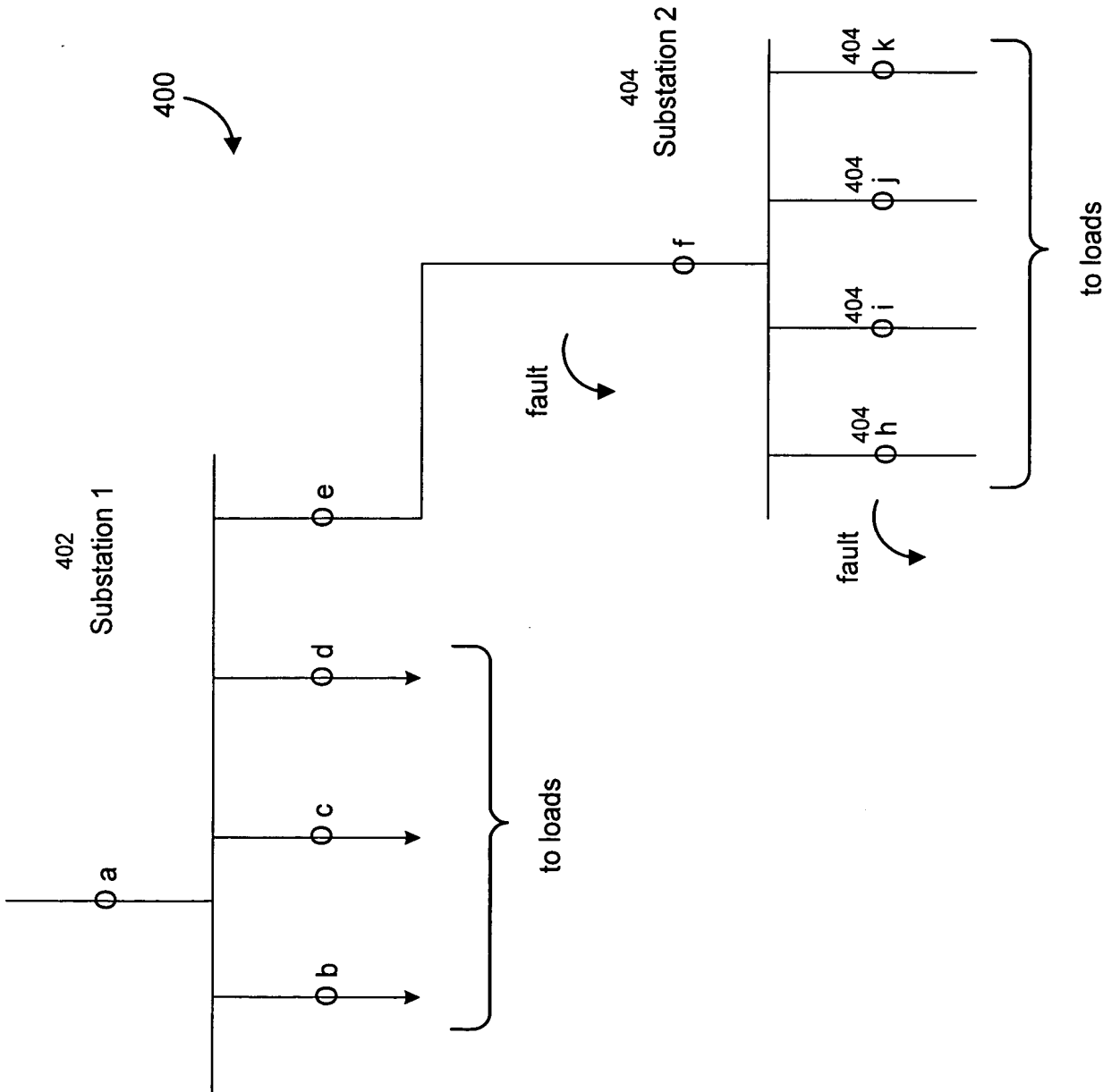


Phasor ION Module Diagram

(3/63)
FIG. 3



(4/63)
FIG. 4



(5/63)
FIG. 5

FIG. 6

(6/63)

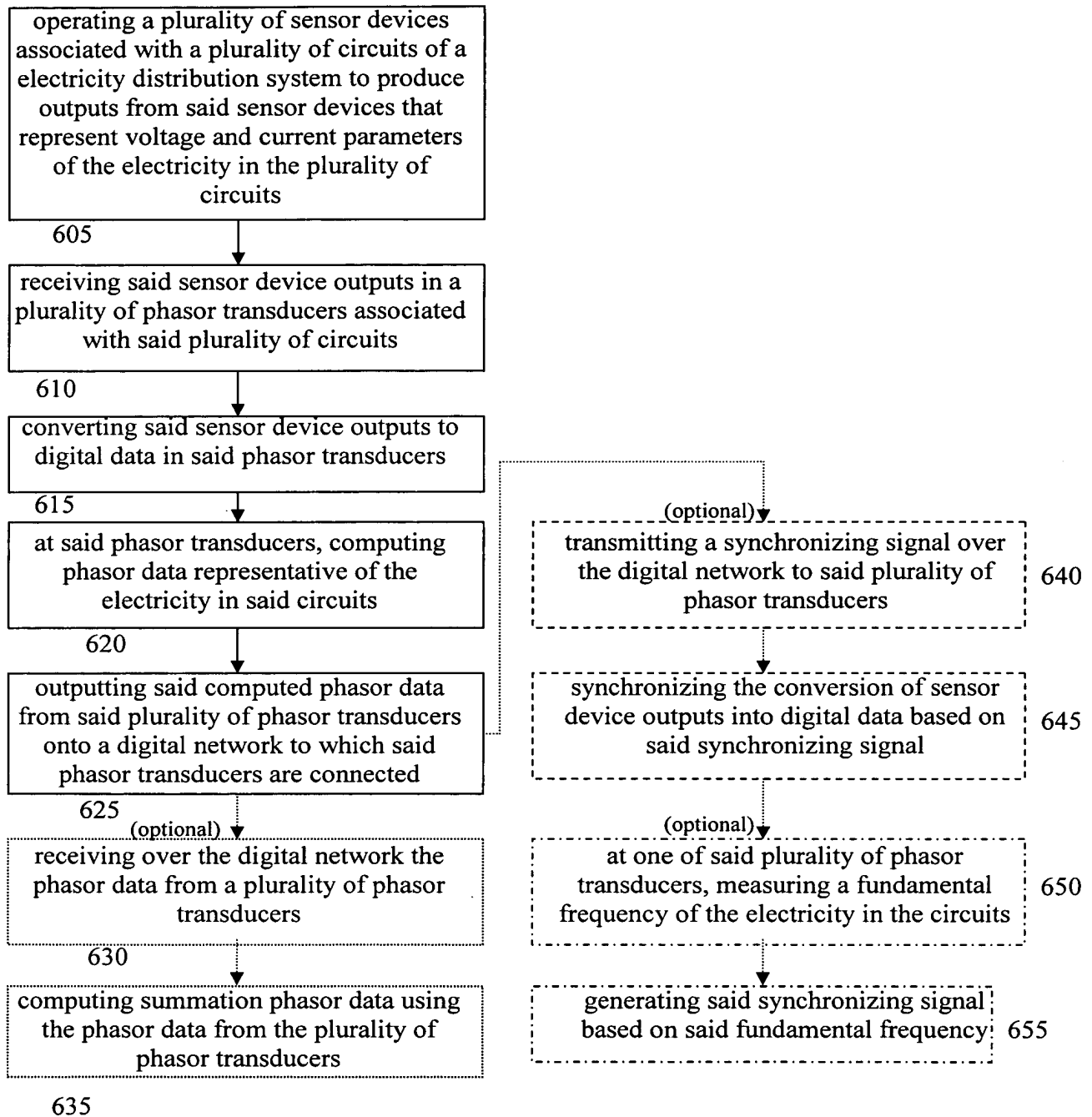
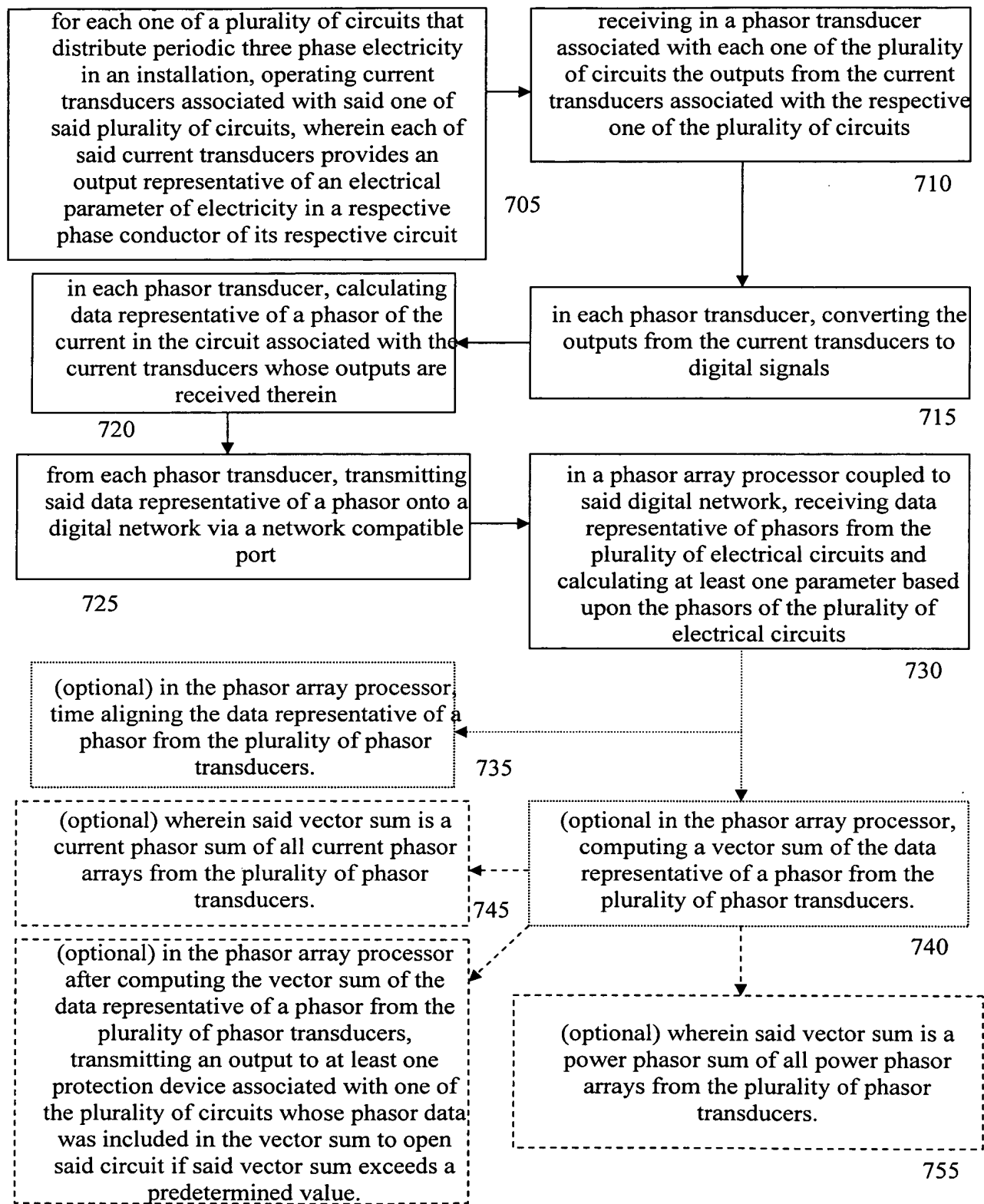


FIG. 7

(7/63)



(9/63)

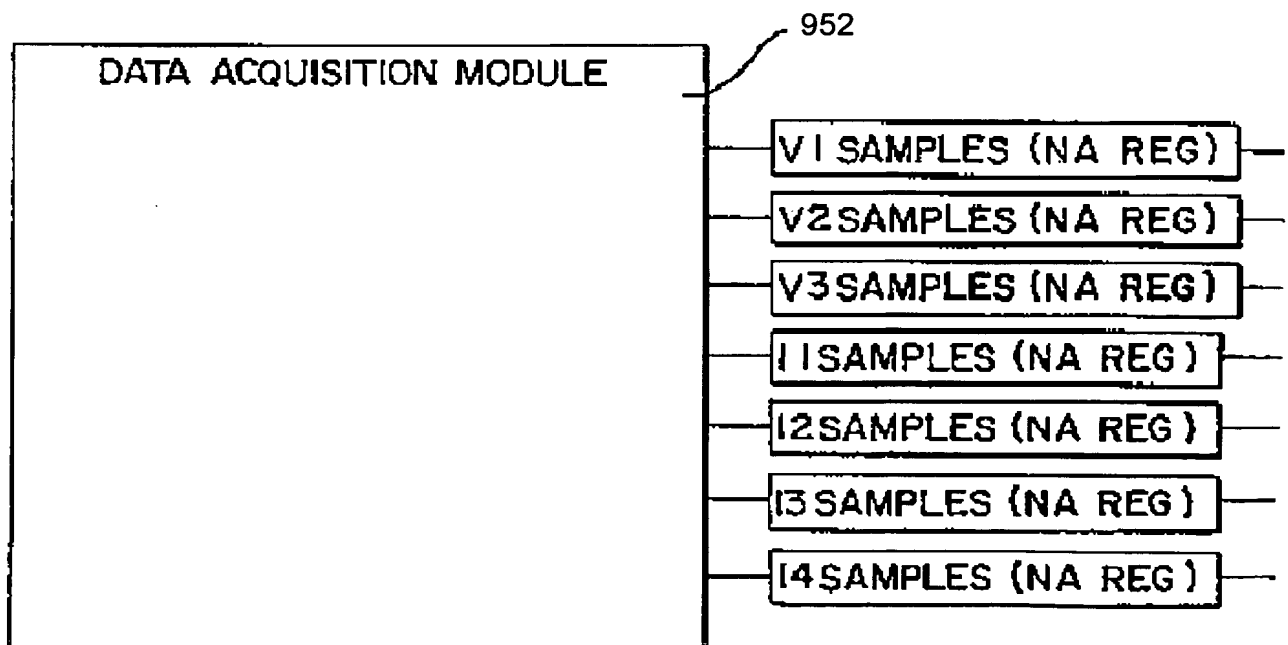
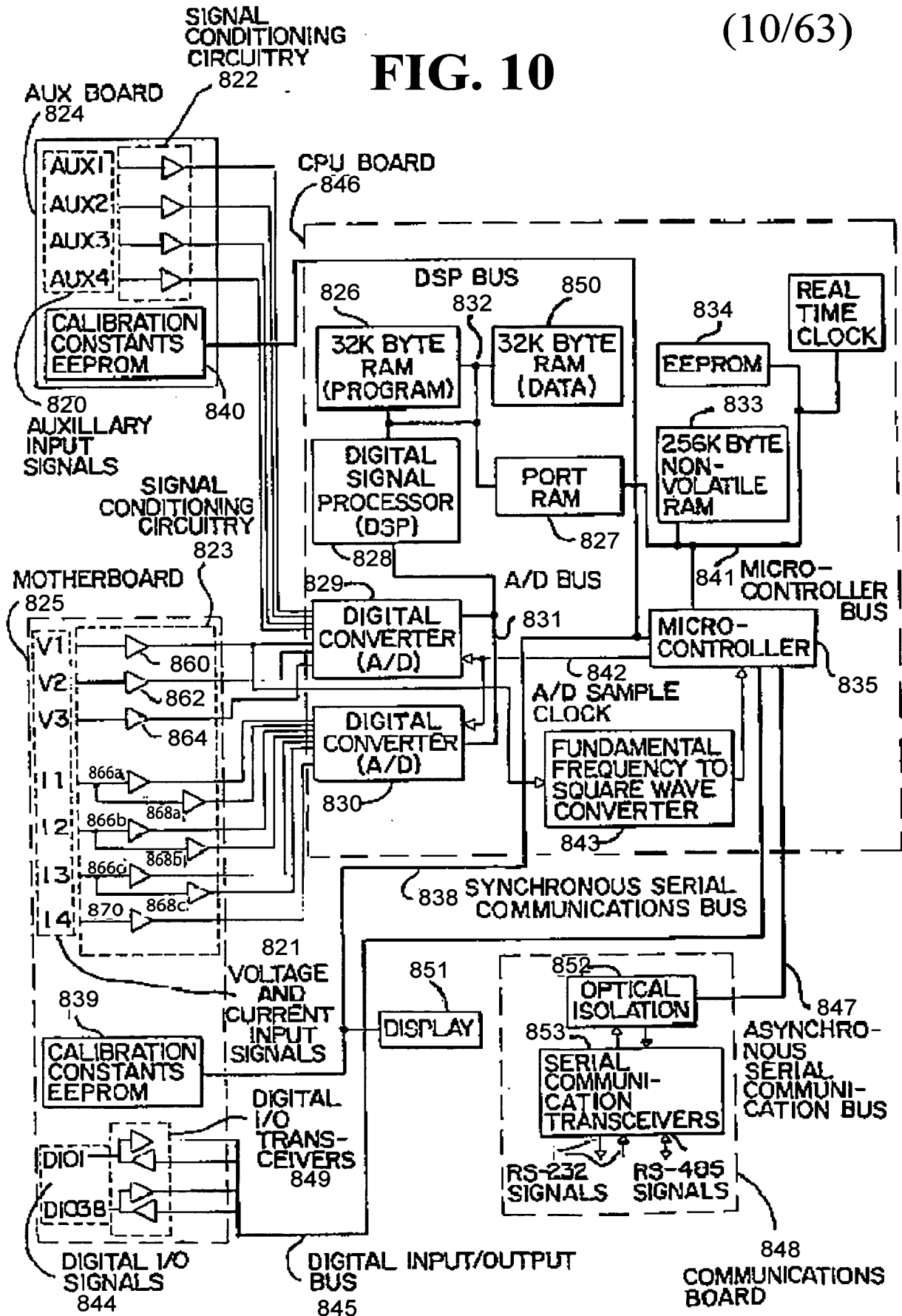


FIG. 10

(10/63)



DATA ACQUISITION
MODULE
OPERATION (CLIENT
PORTION)

(11/63)

FIG. 11A

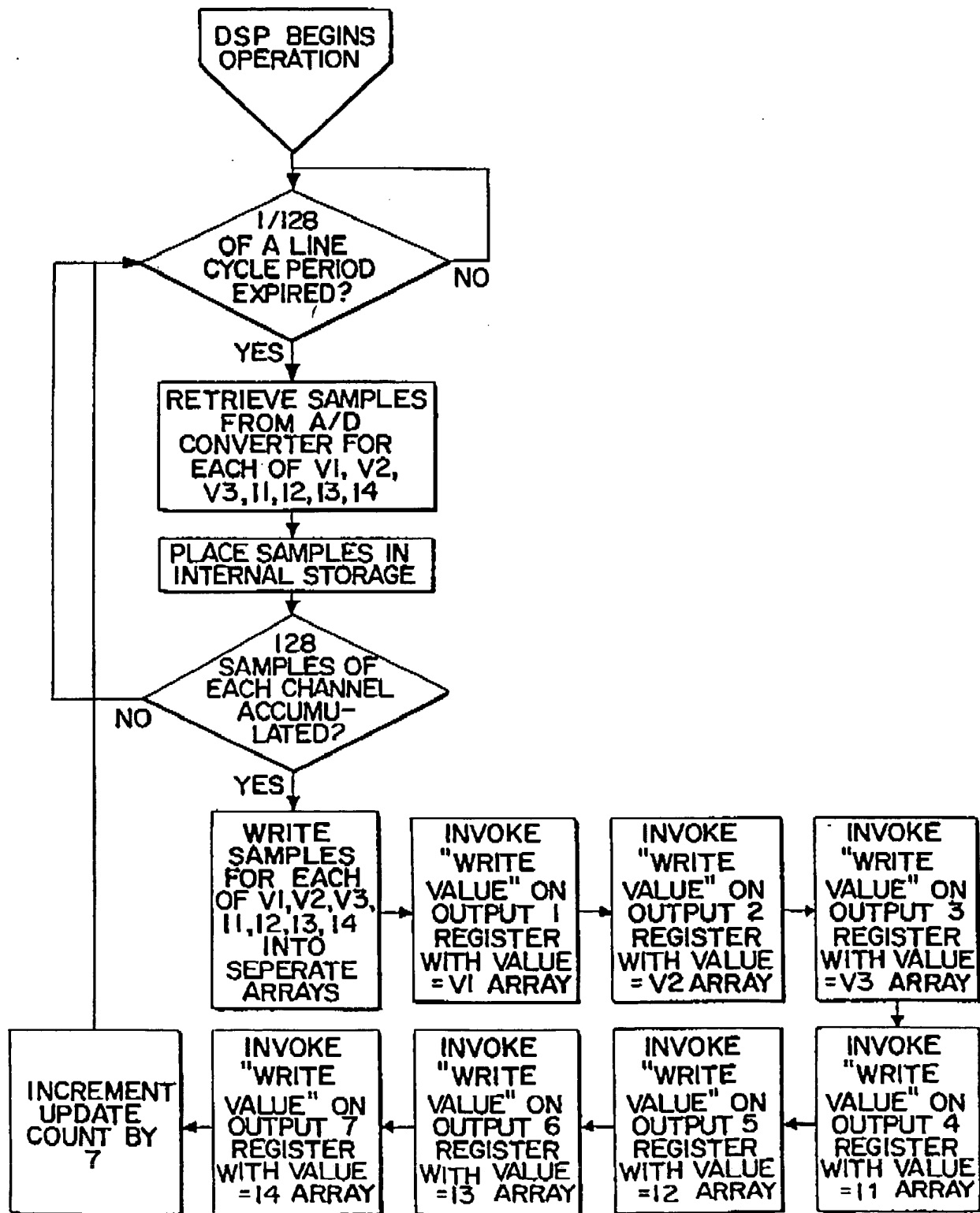
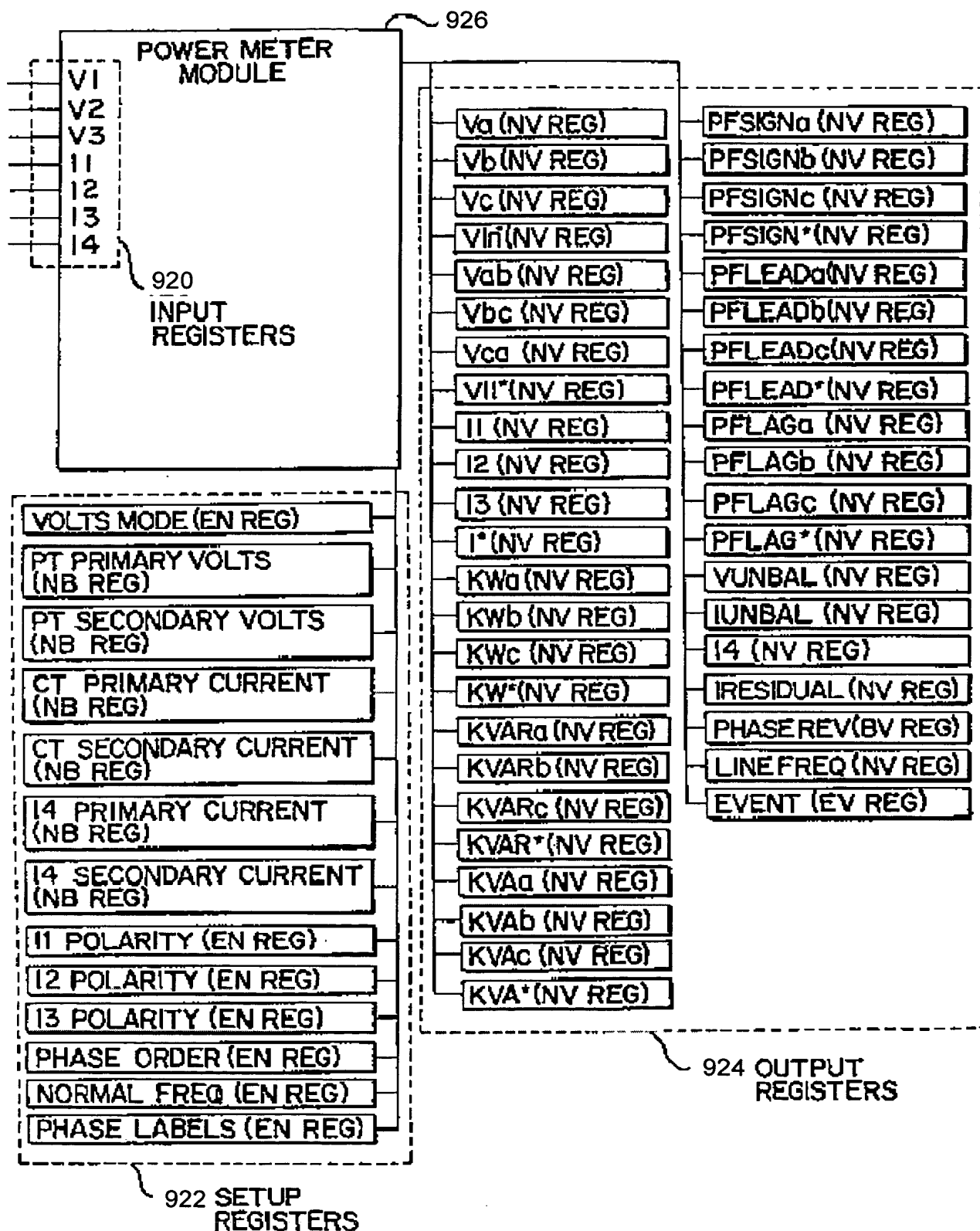


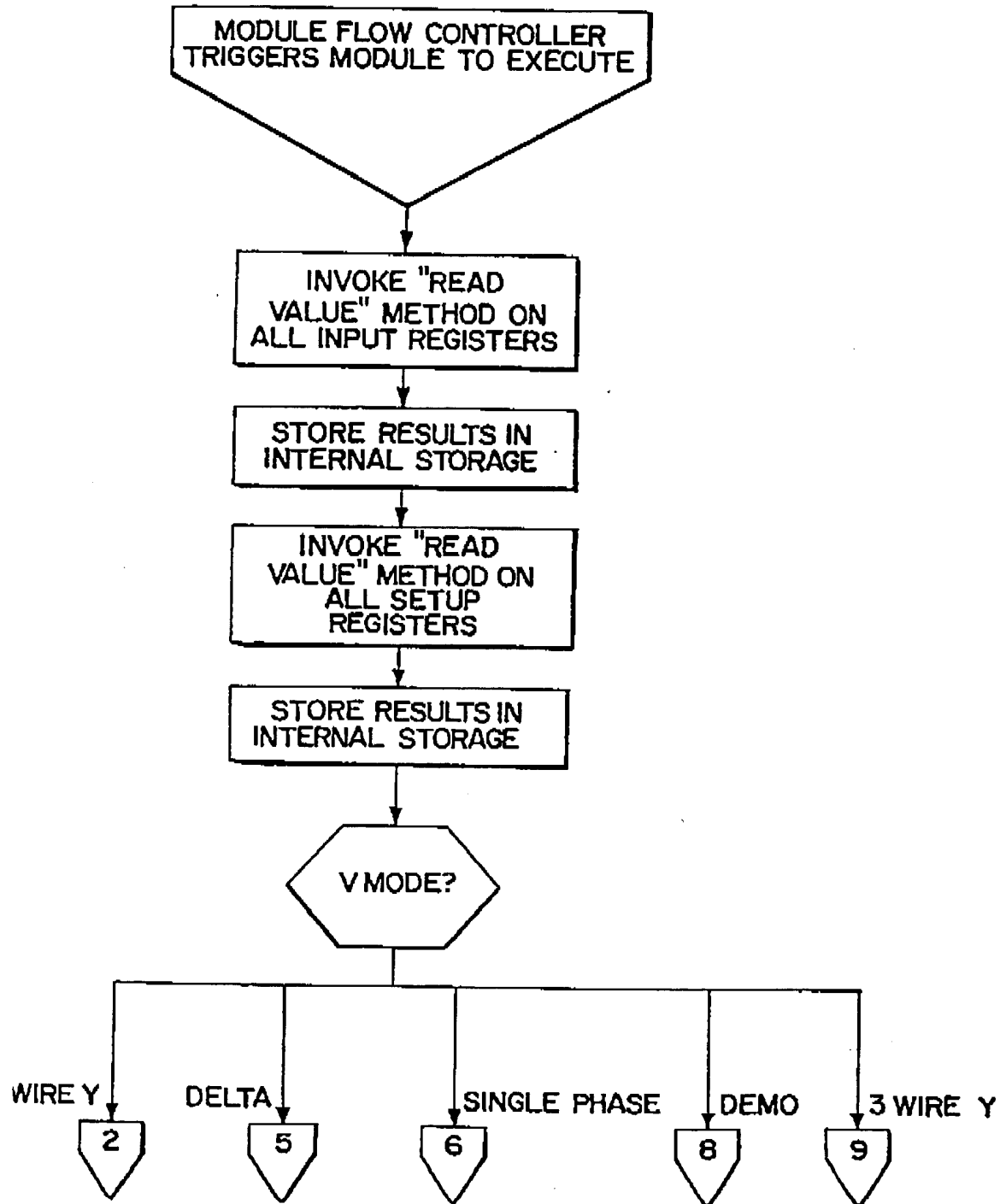
FIG. 12

(12/63)



POWER METER
MODULE
OPERATION
(CLIENT PORTION)

FIG. 12A (13/63)



(14/63)



FIG. 12B

(15/63)

FIG. 12C

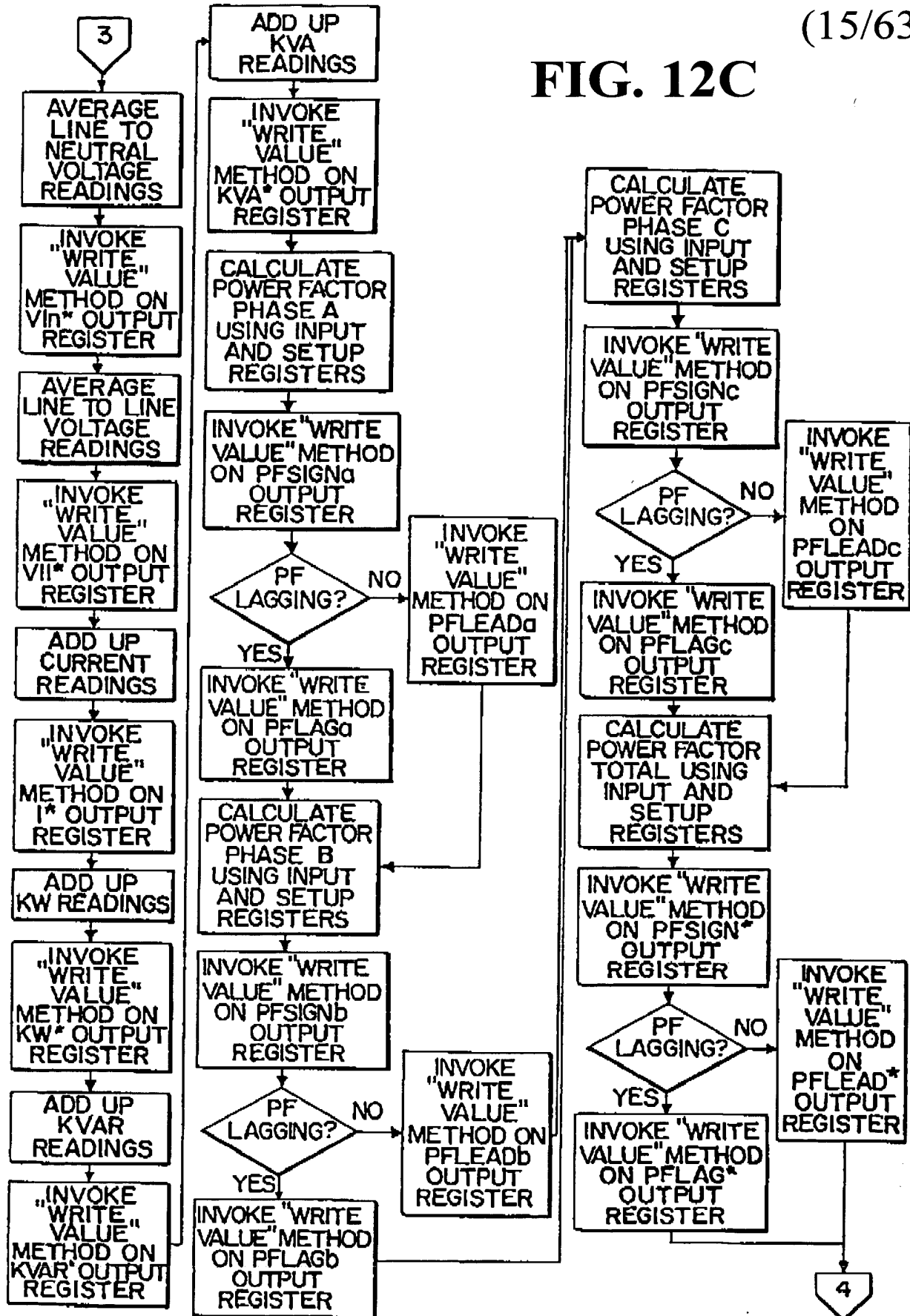


FIG. 12D

(16/63)

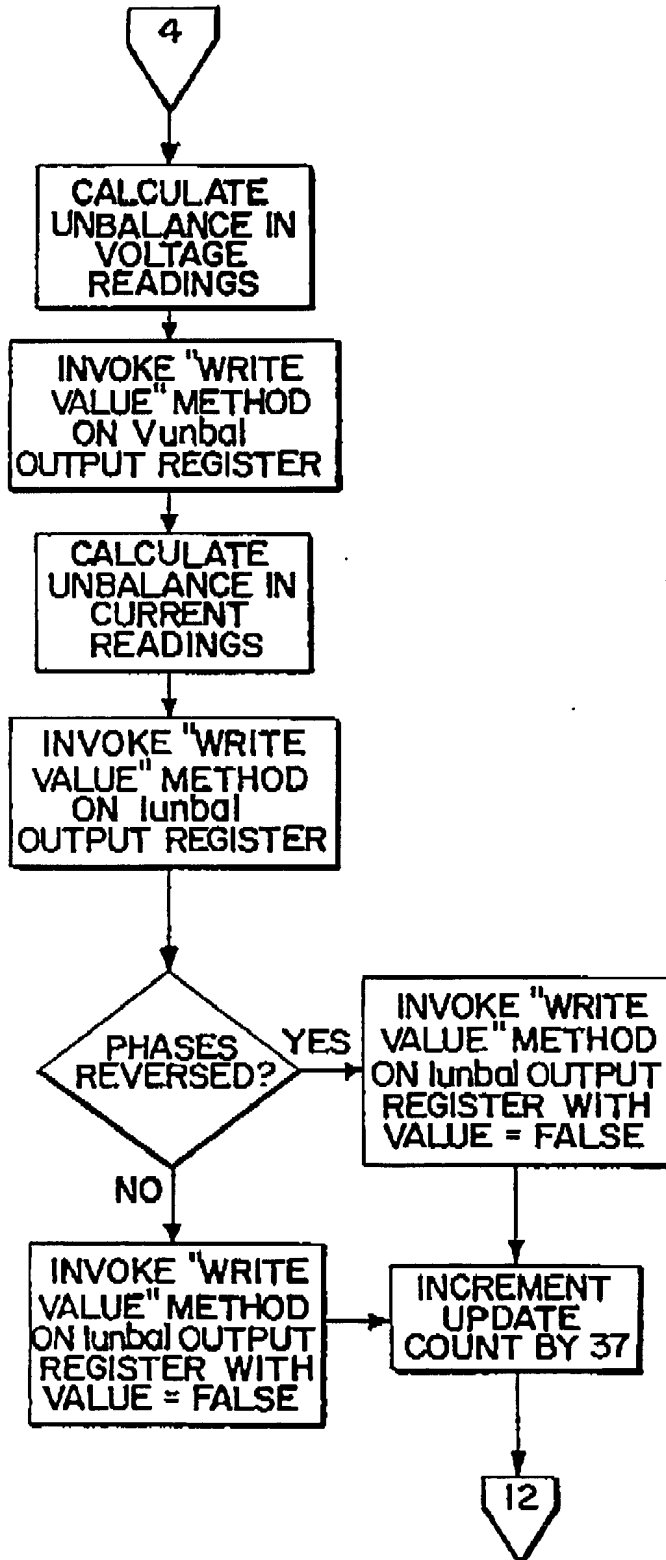


FIG. 12H

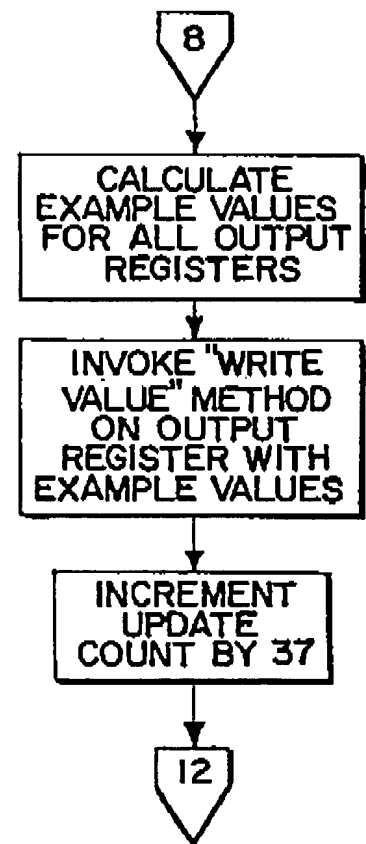


FIG. 12E

(17/63)

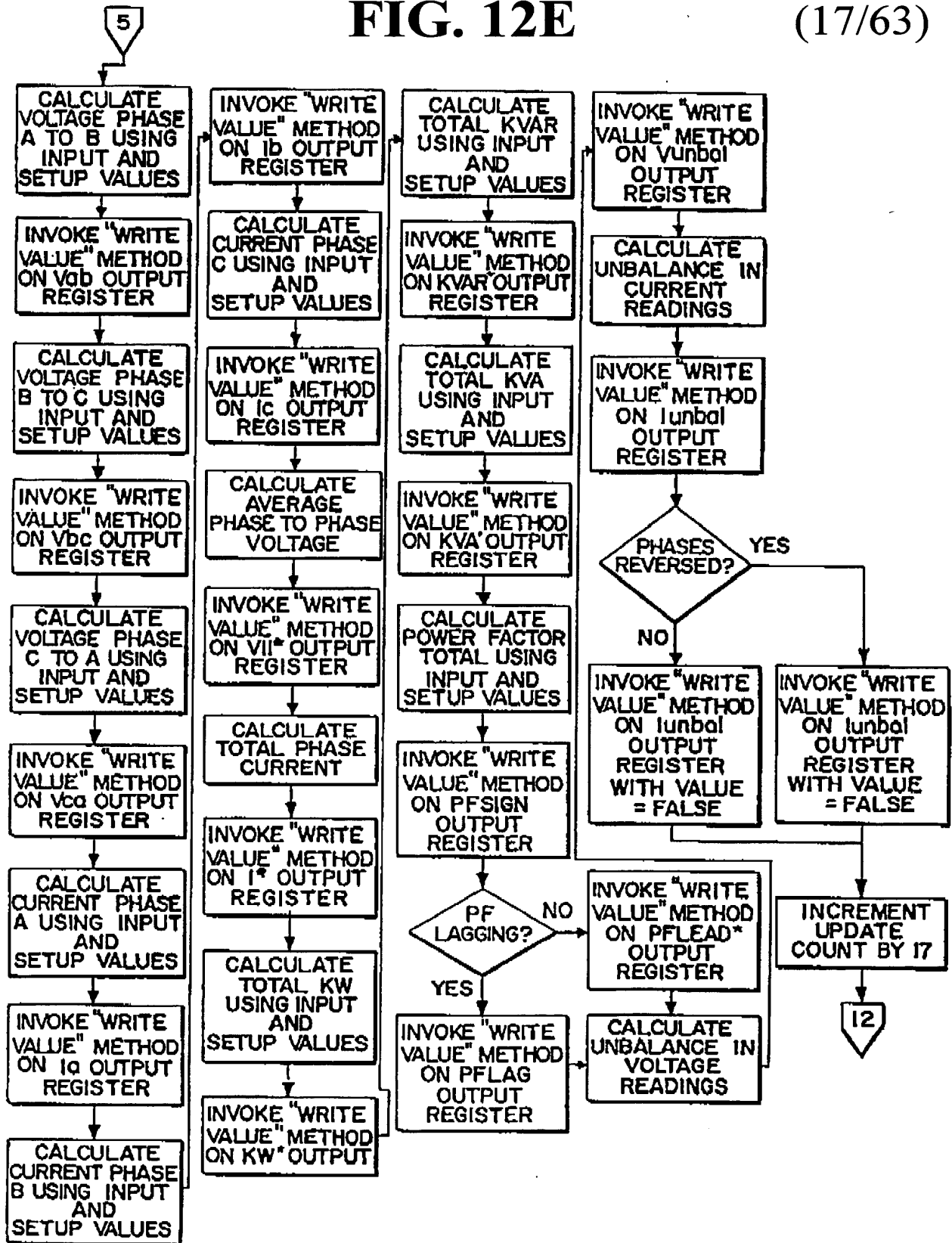


FIG. 12F

(18/63)

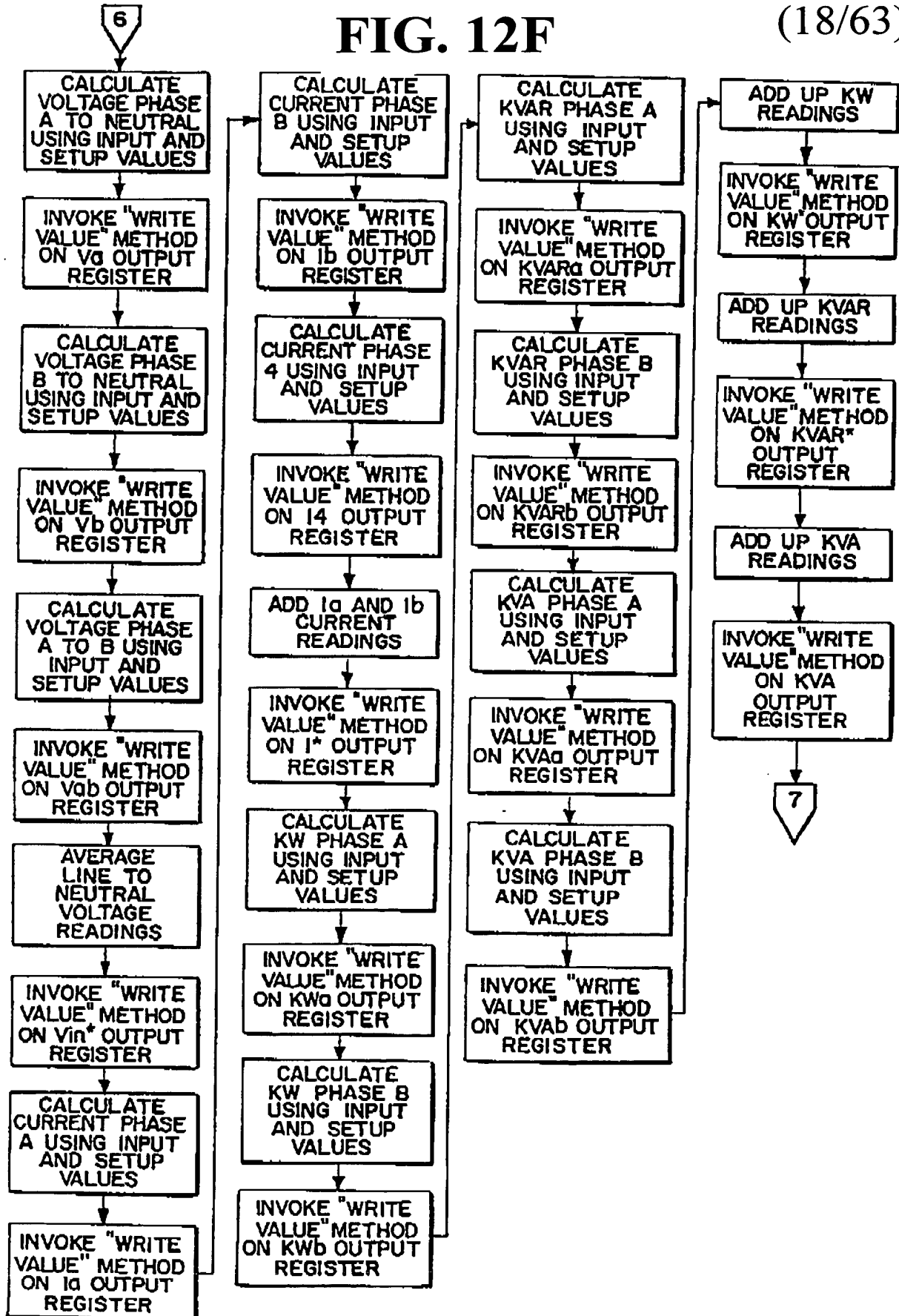
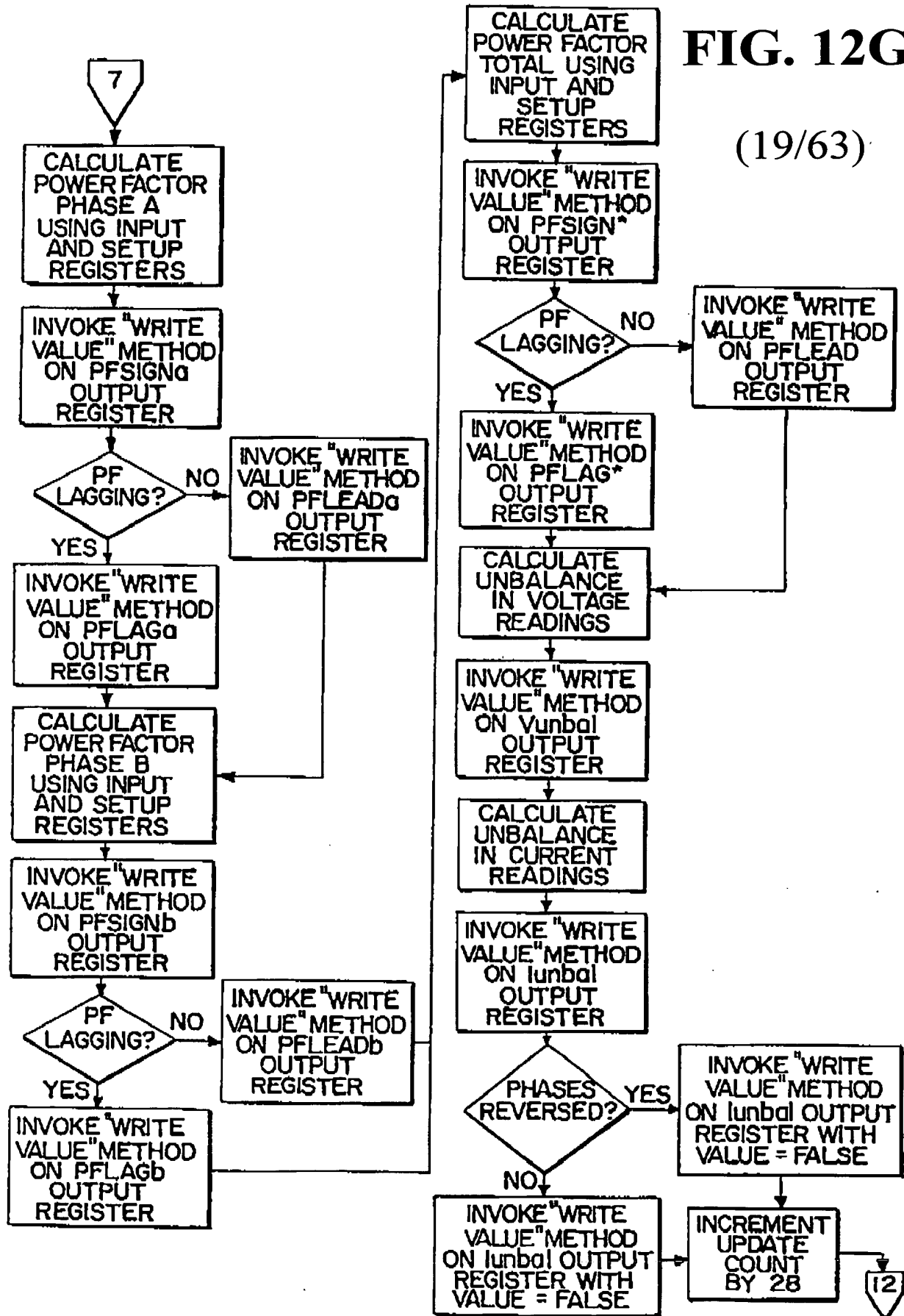


FIG. 12G

(19/63)



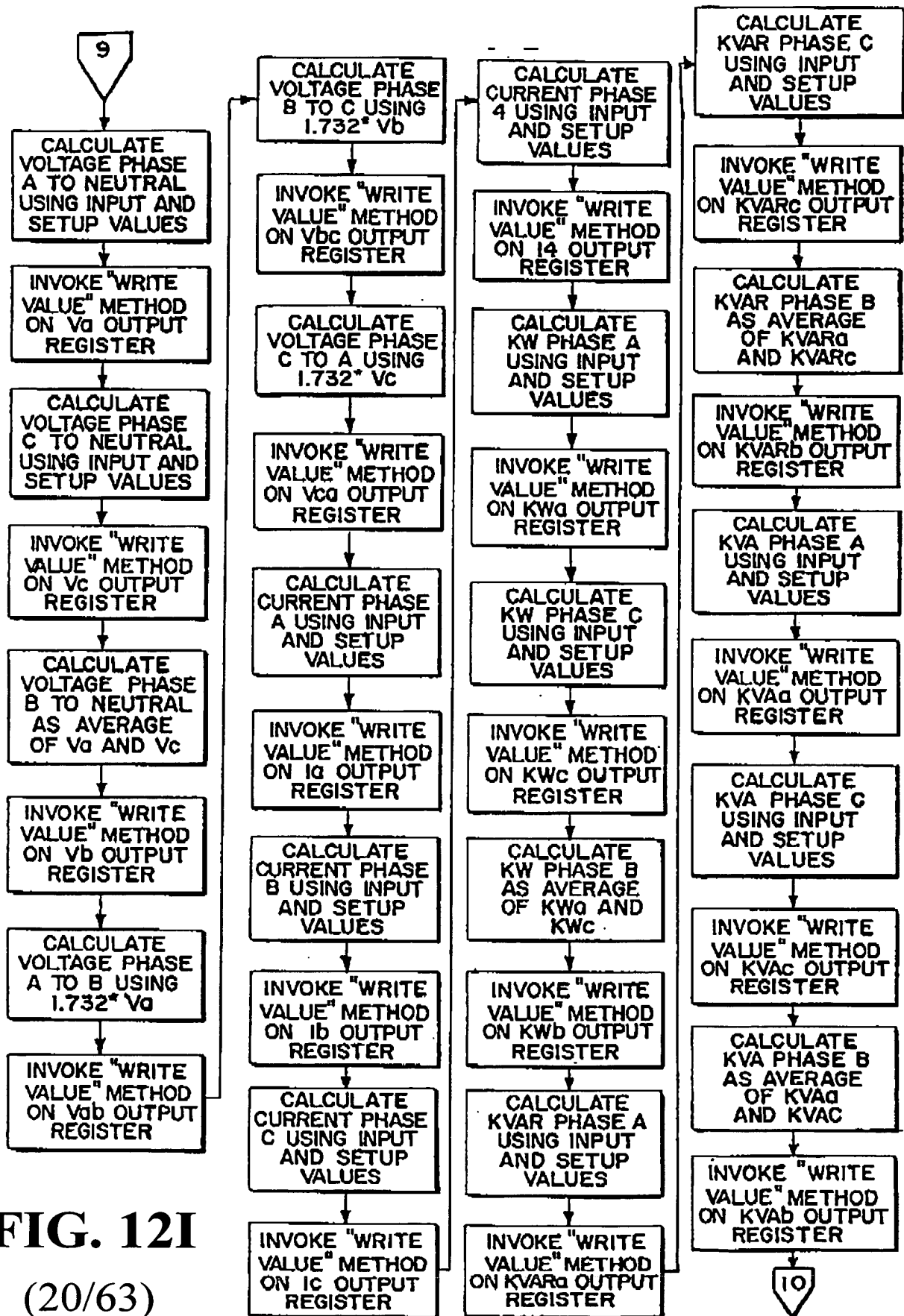


FIG. 12J (21/63)

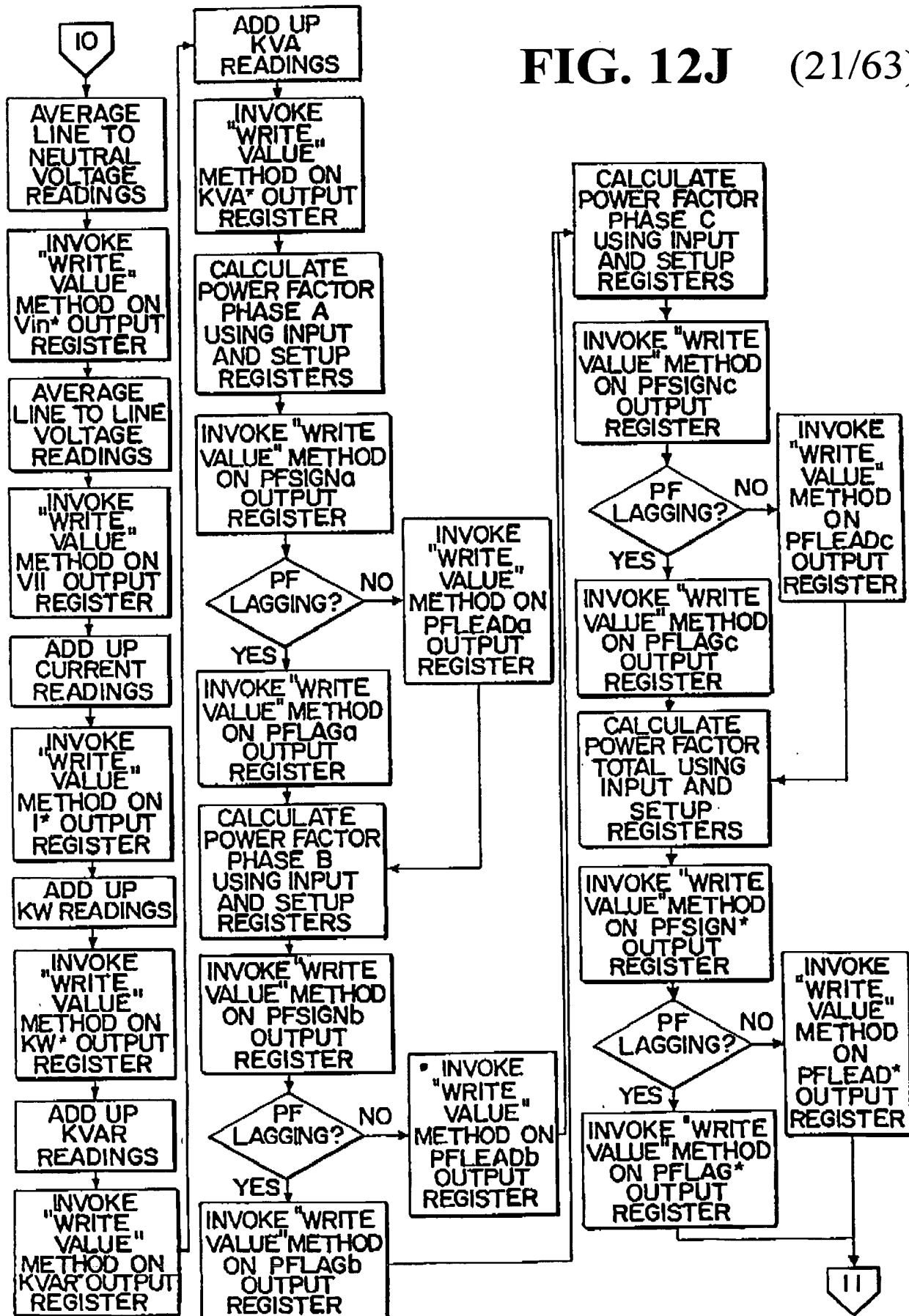
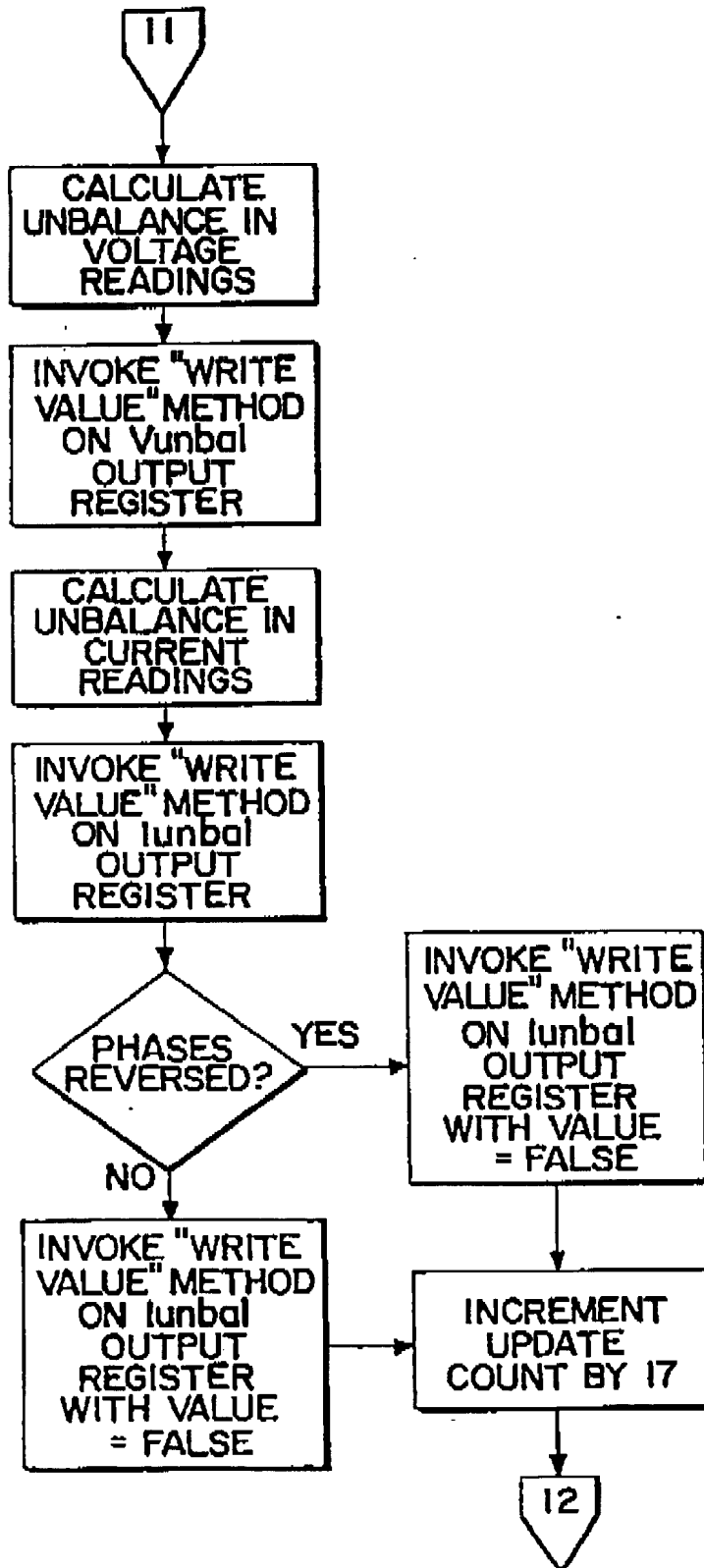


FIG. 12K



(22/63)

FIG. 12L

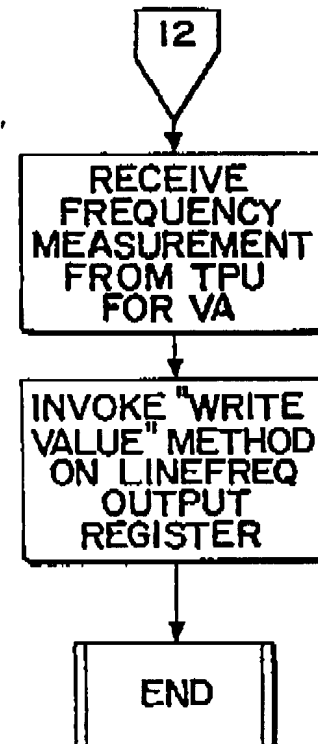


FIG. 13 (23/63)

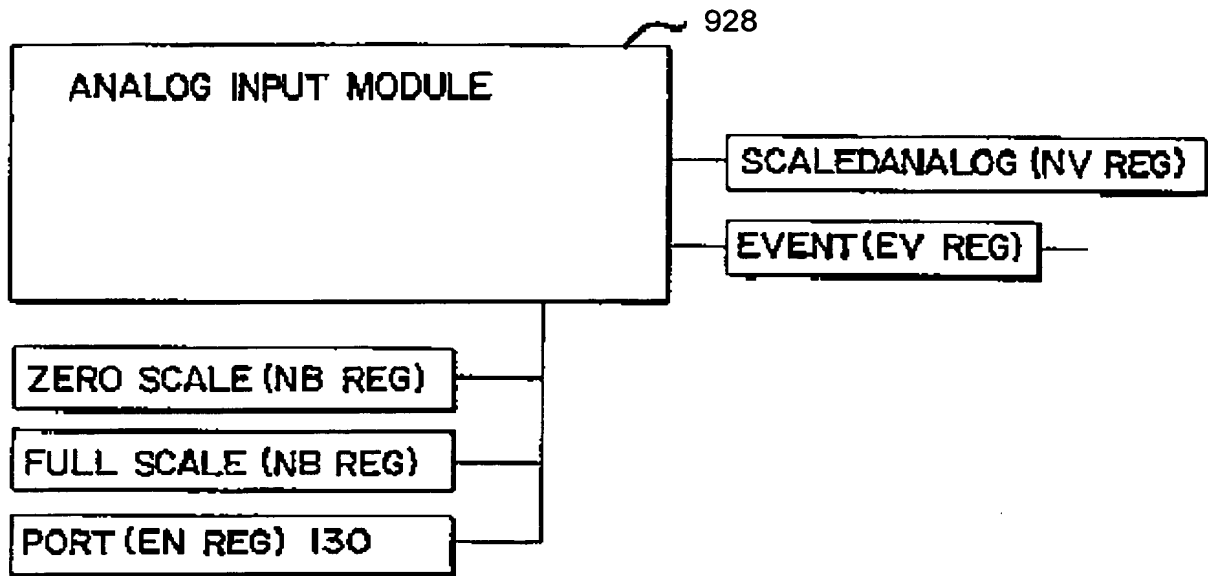


FIG. 14

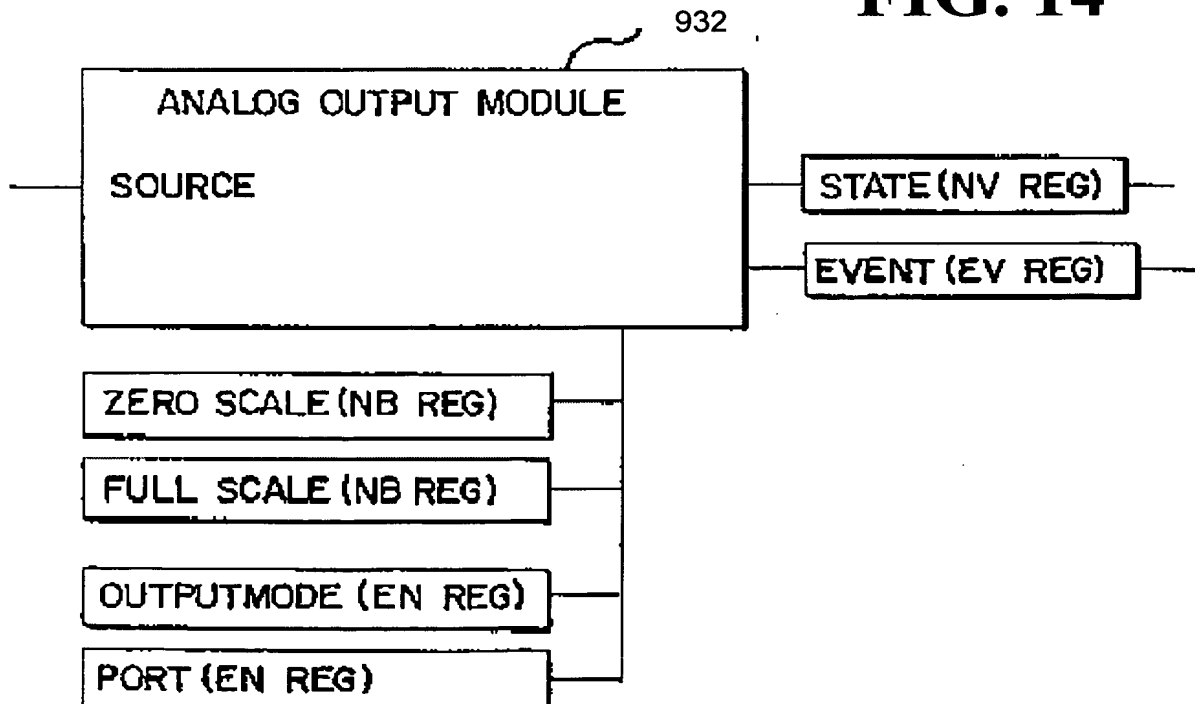


FIG. 13A

(24/63)

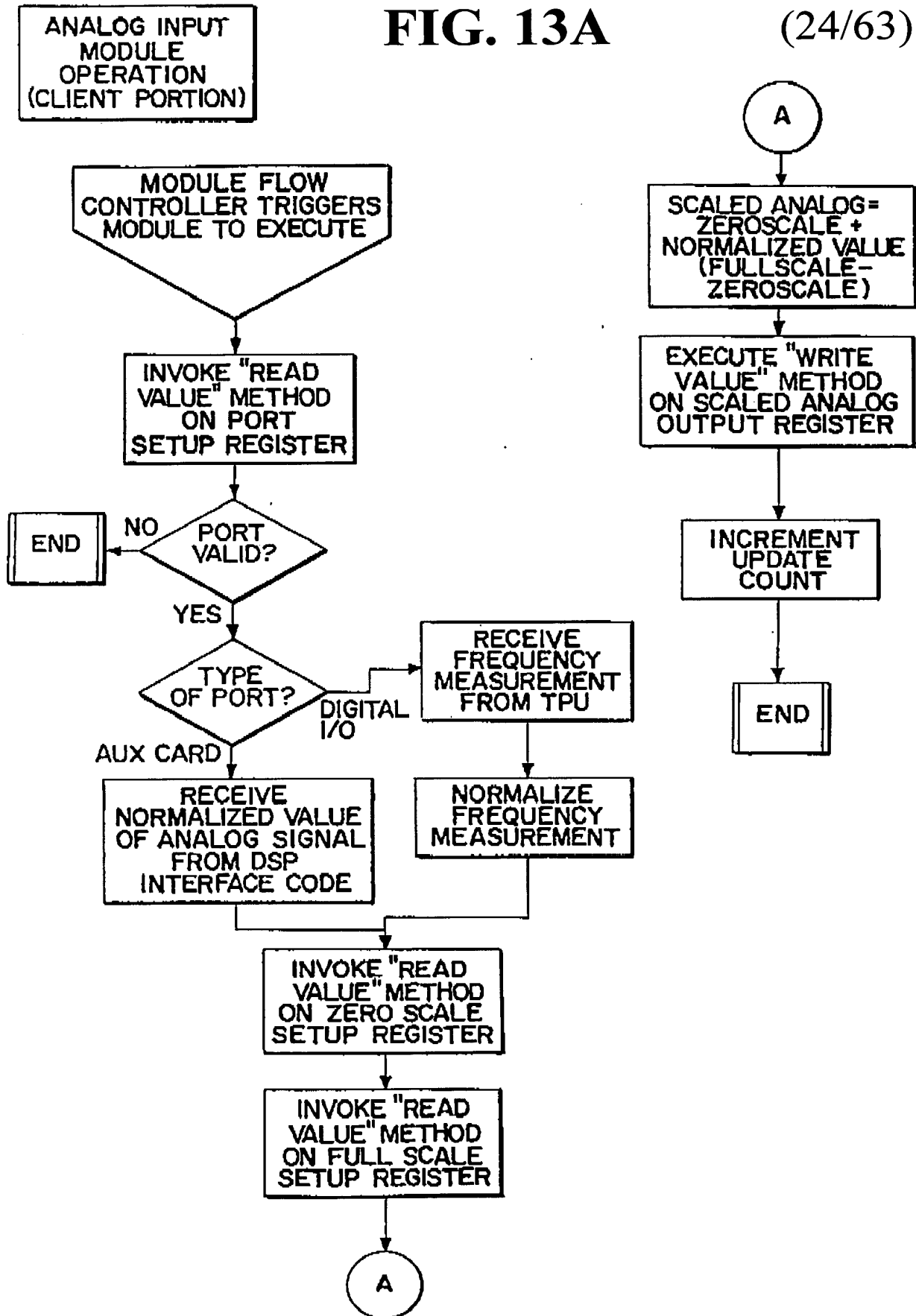


FIG. 14A

(25/63)

ANALOG OUTPUT
MODULE
OPERATION
(CLIENT PORTION)

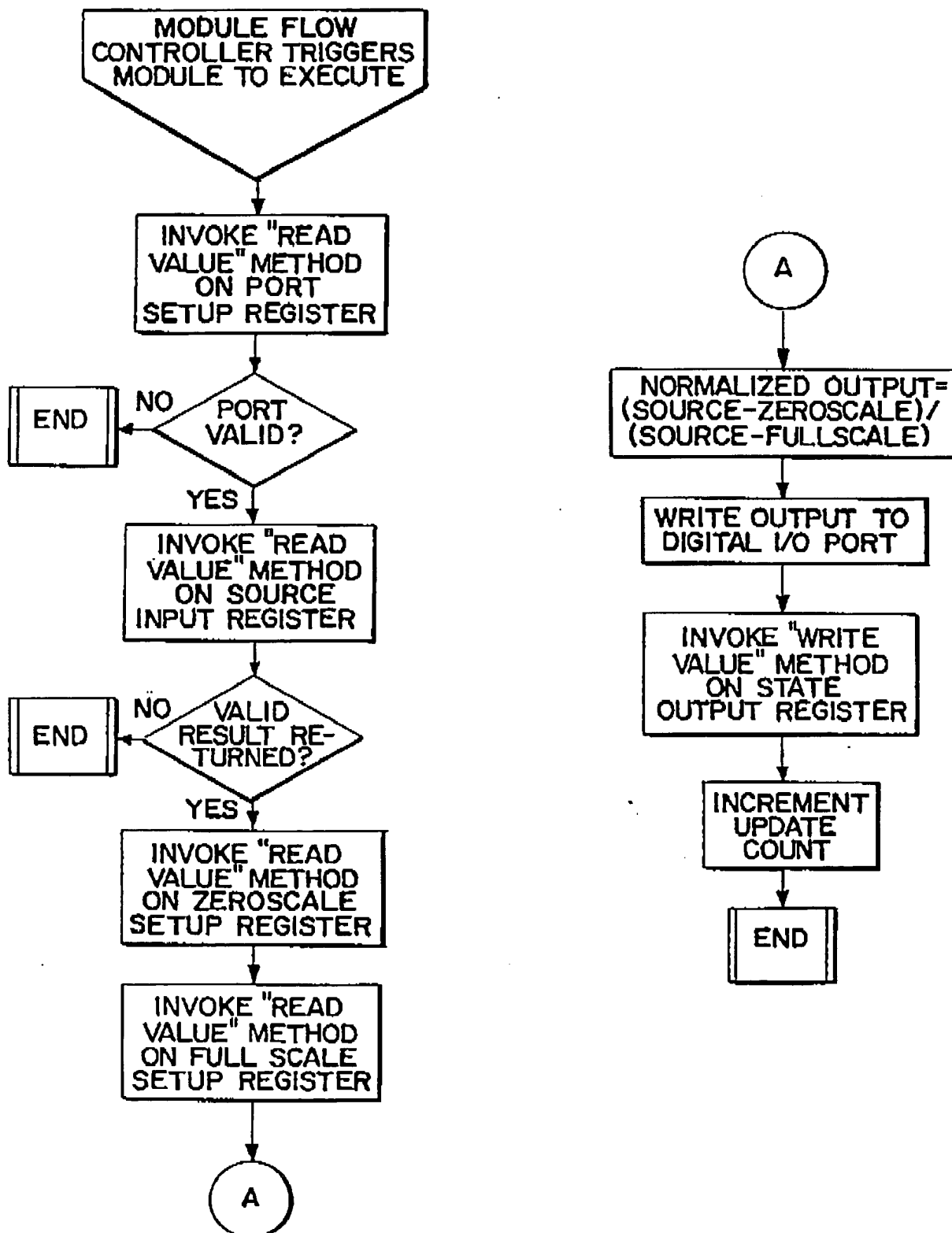


FIG. 15

(26/63)

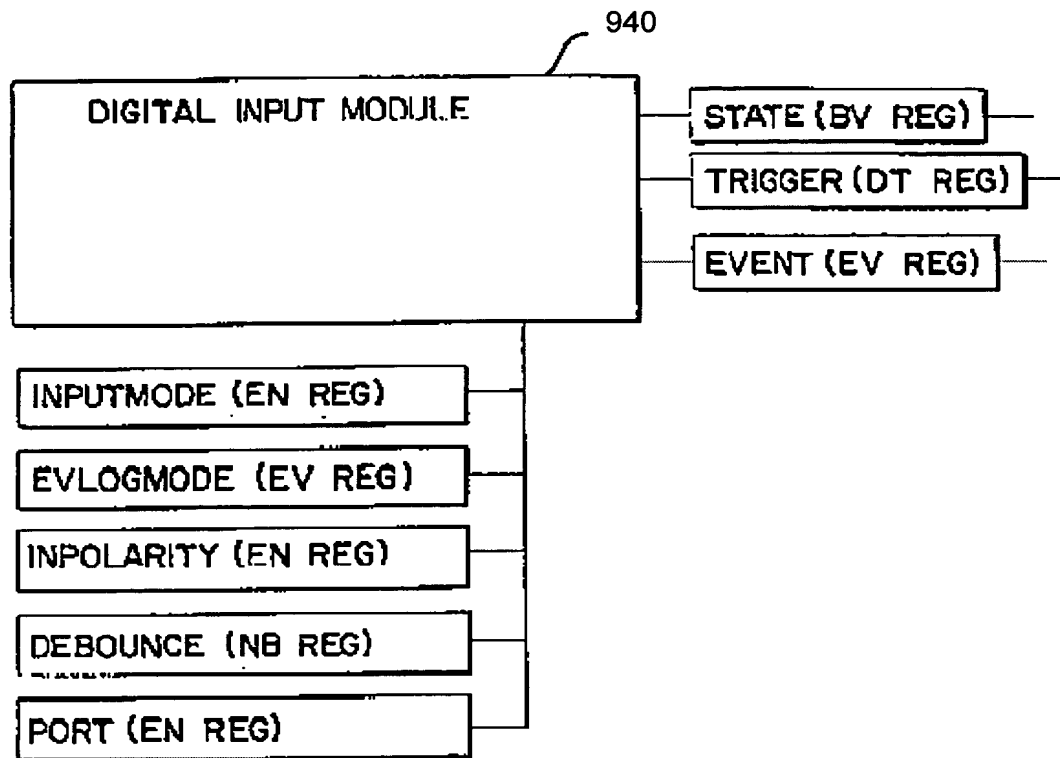


FIG. 16

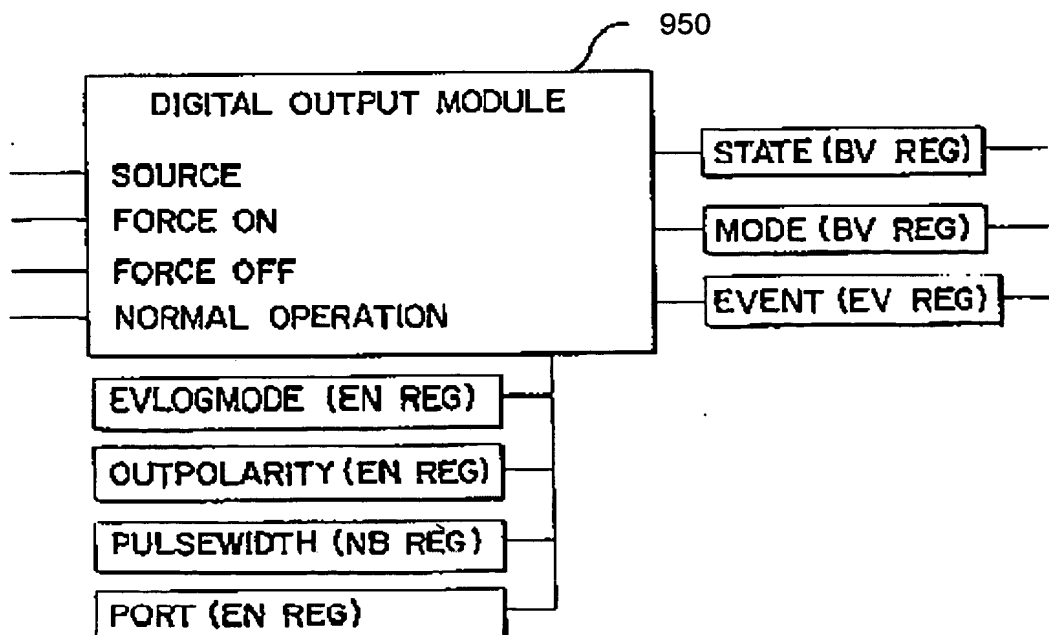


FIG. 15A

(27/63)

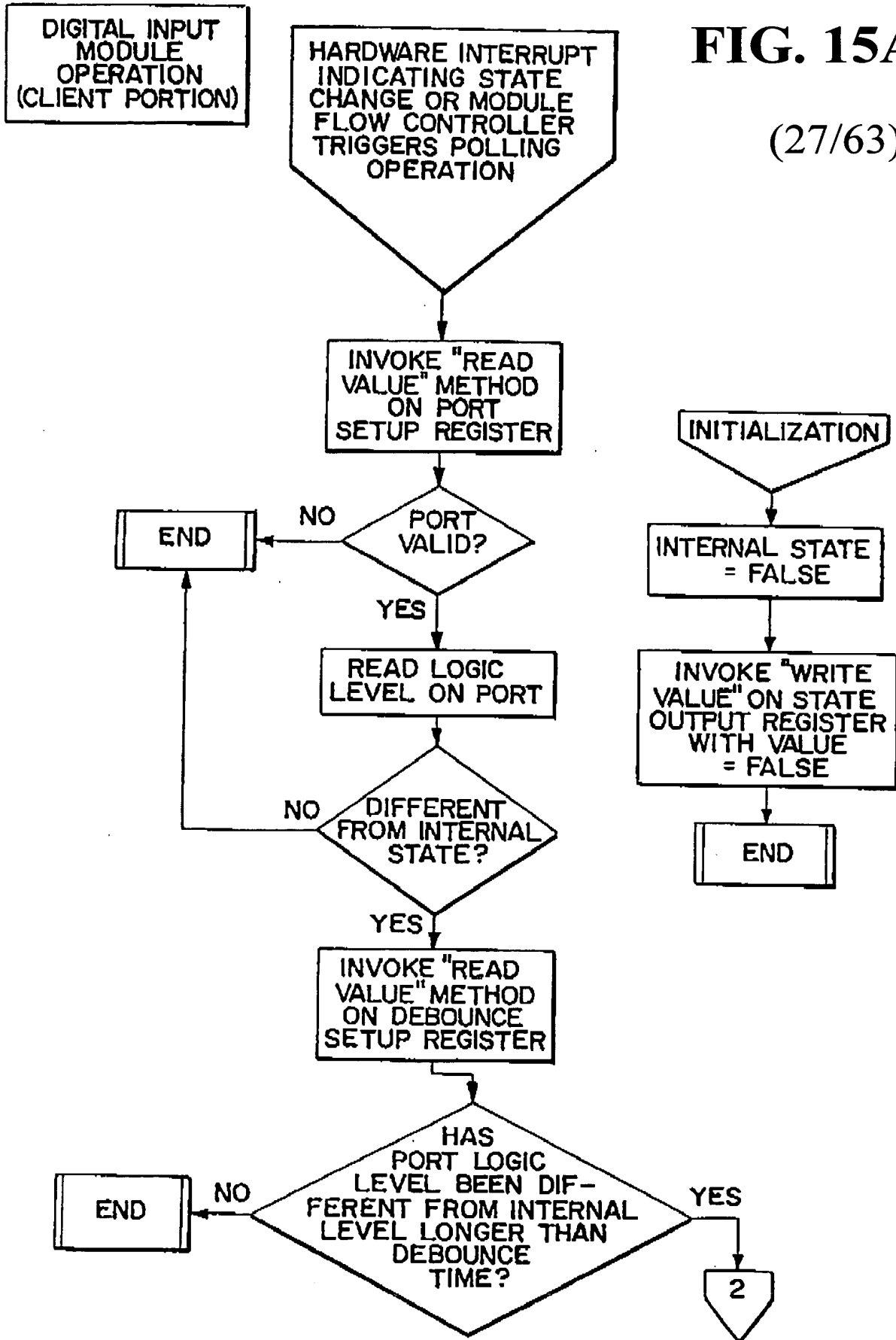


FIG. 15B

(28/63)

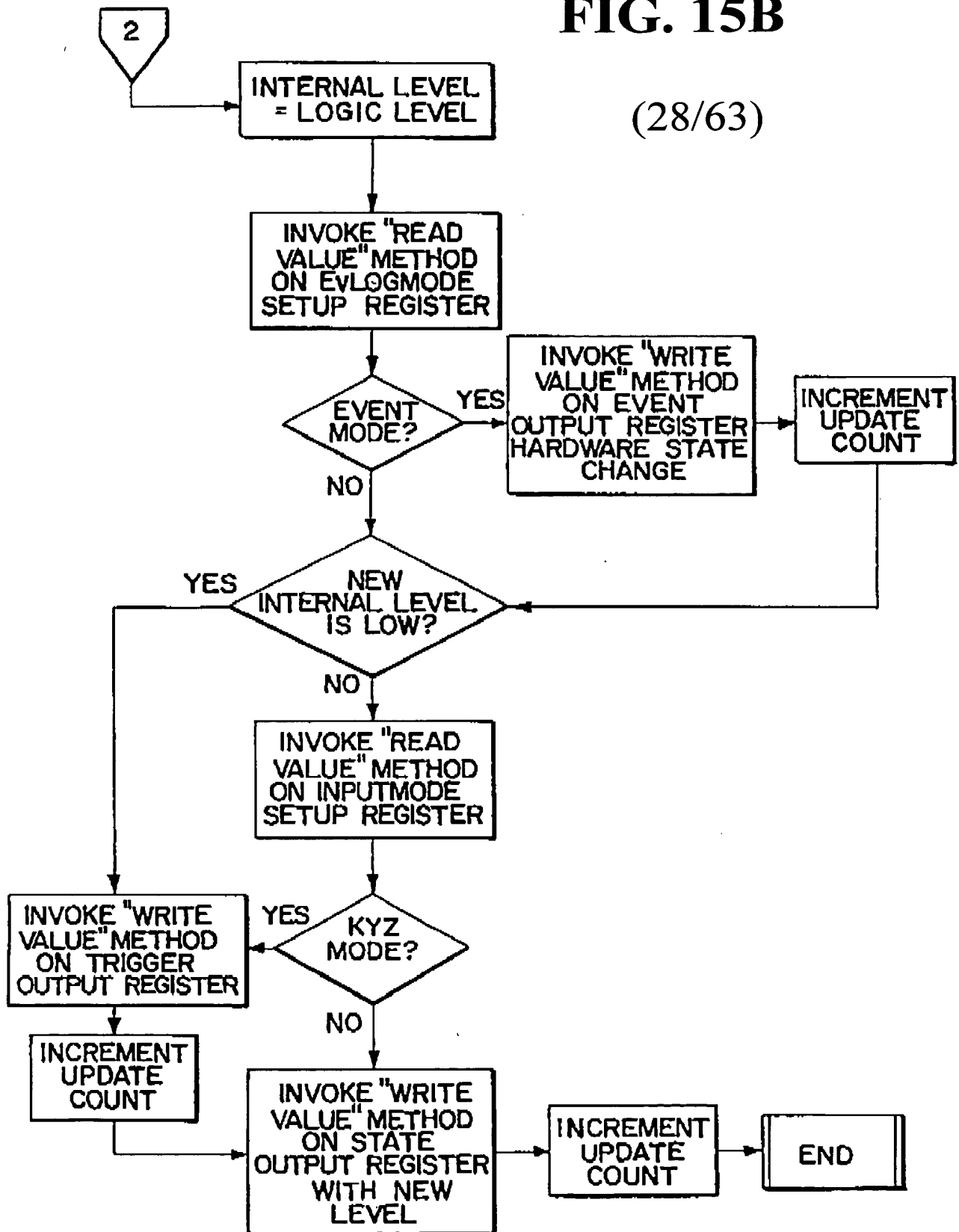


FIG. 16A

(29/63)

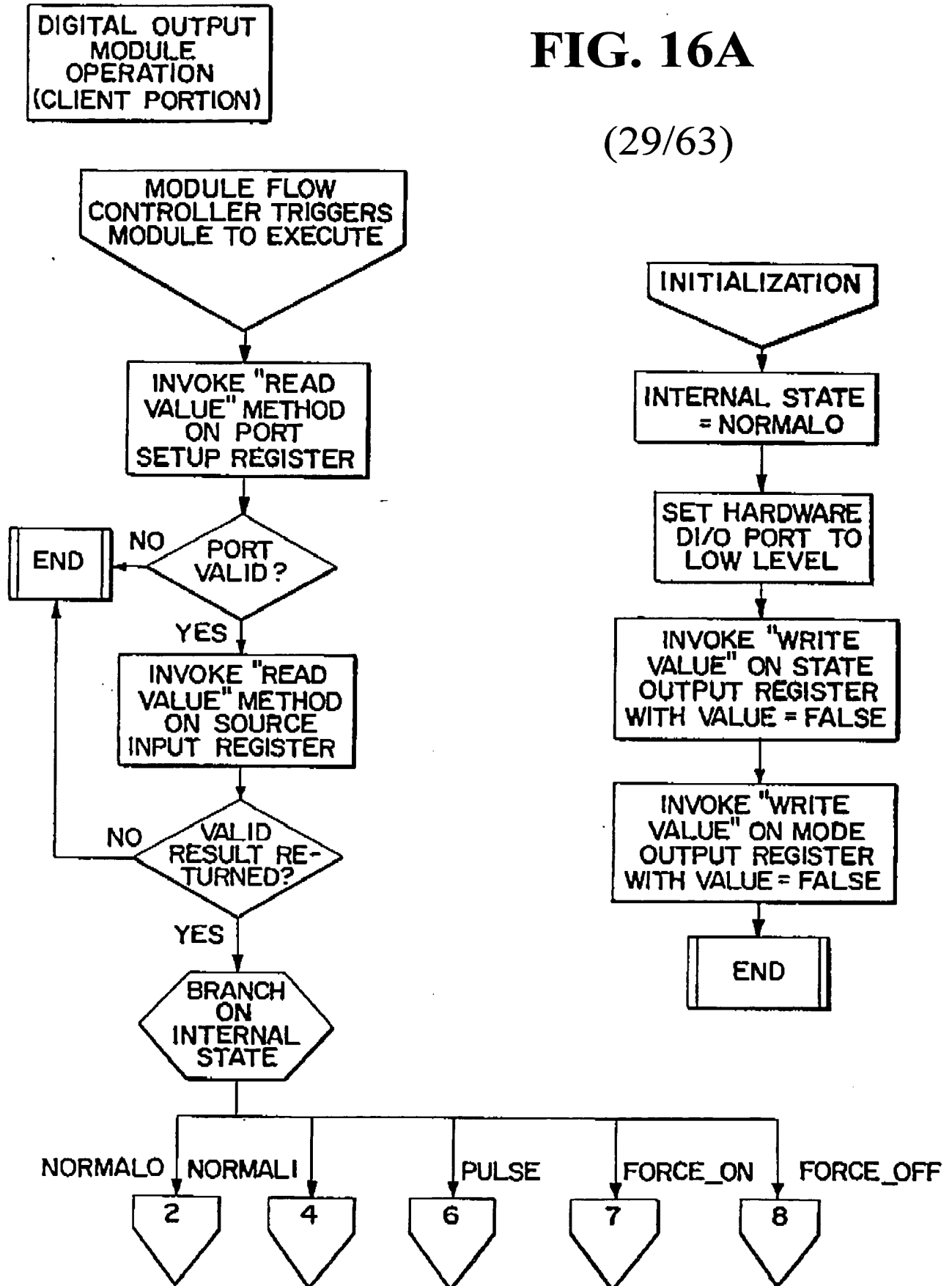


FIG. 16B

(30/63)

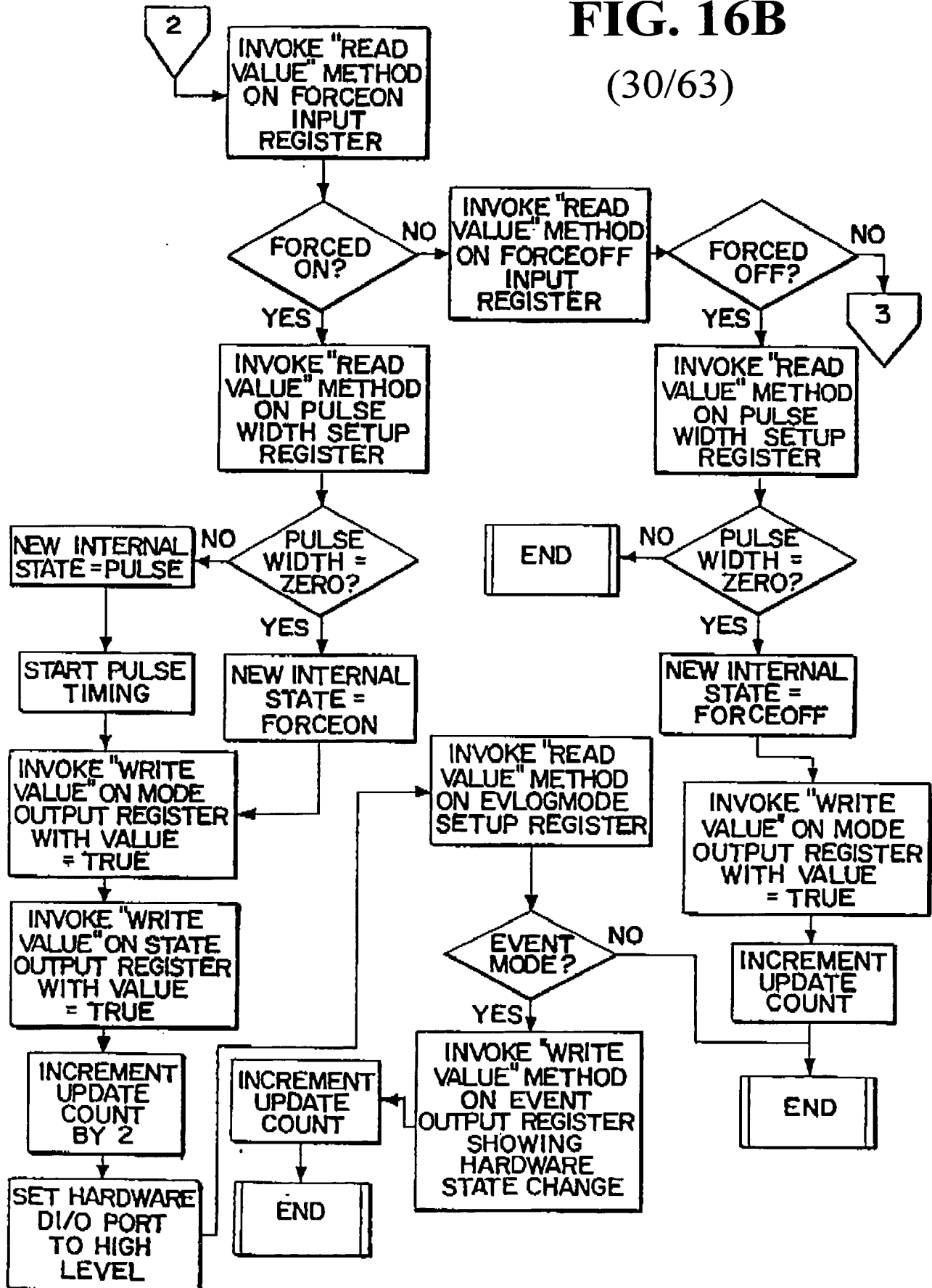


FIG. 16C

(31/63)

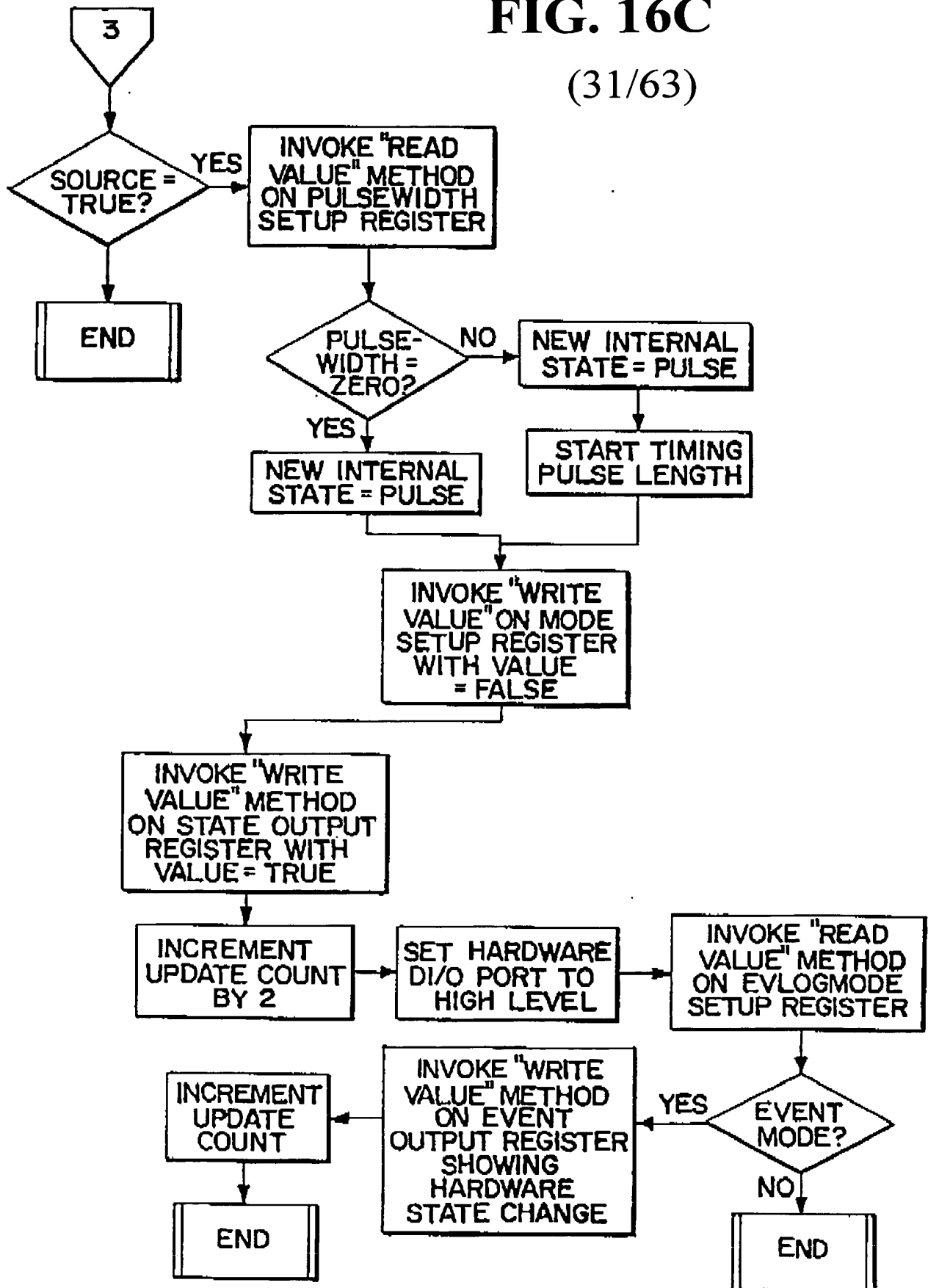


FIG. 16D
(32/63)

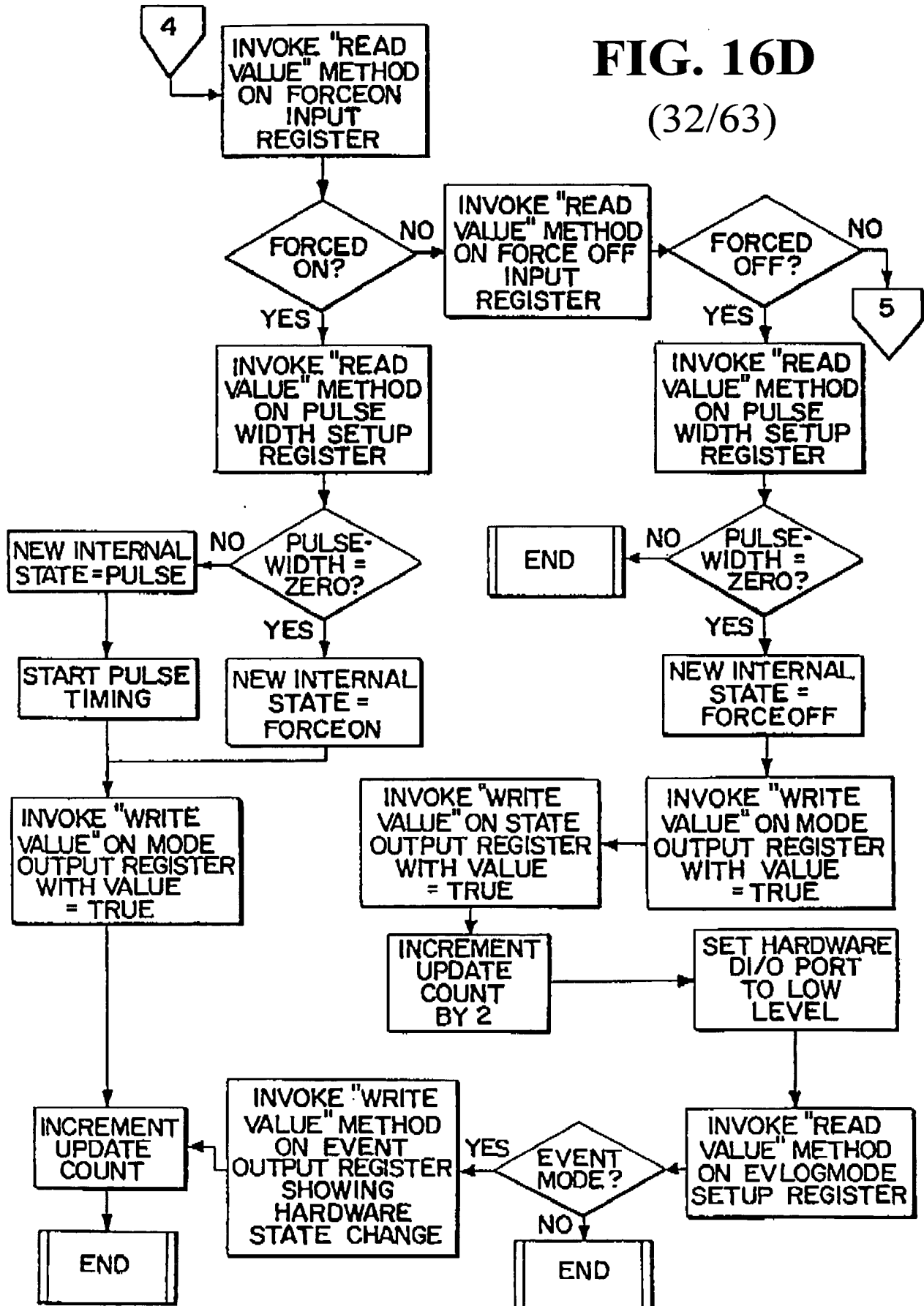


FIG. 16E

(33/63)

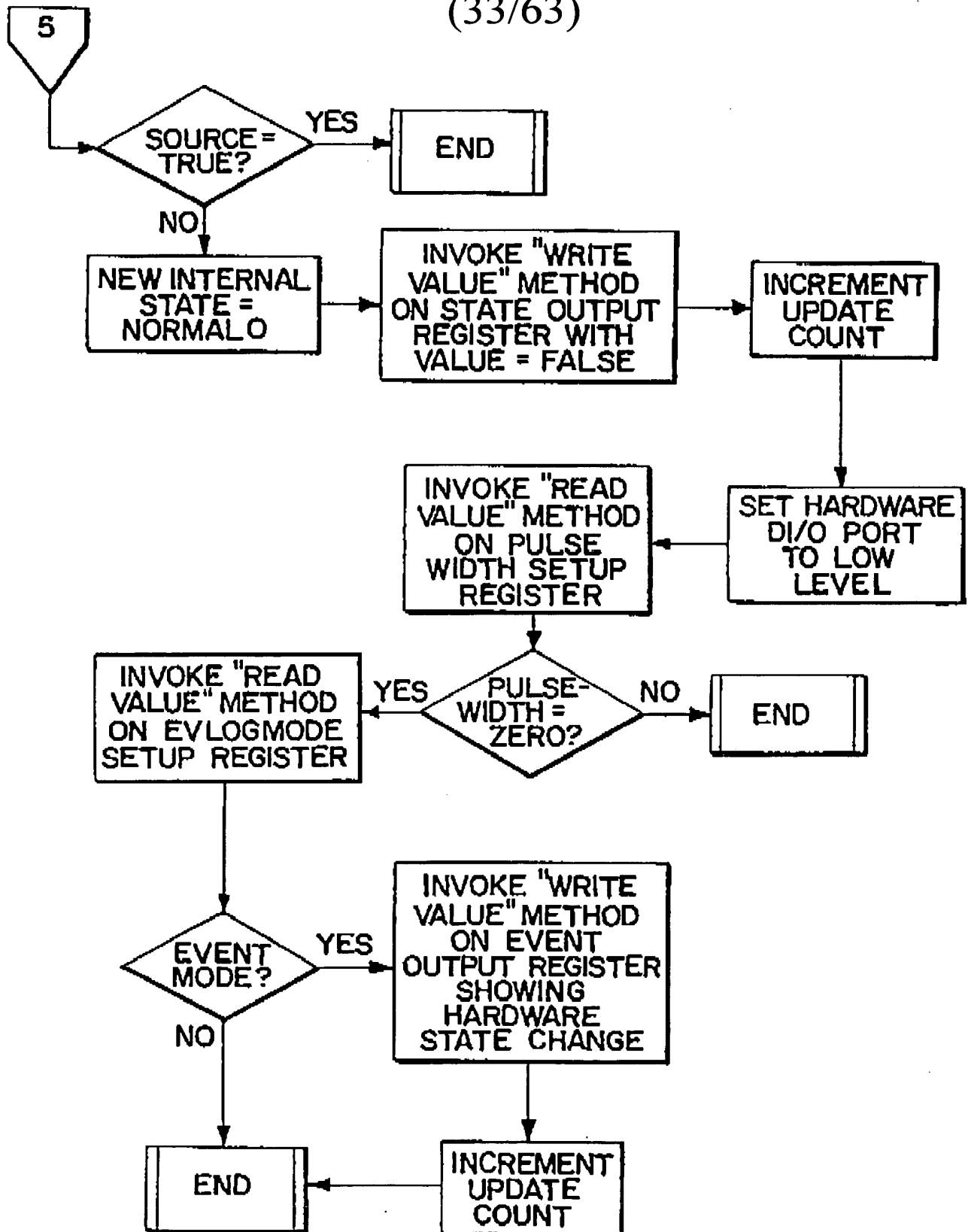


FIG. 16F

(34/63)

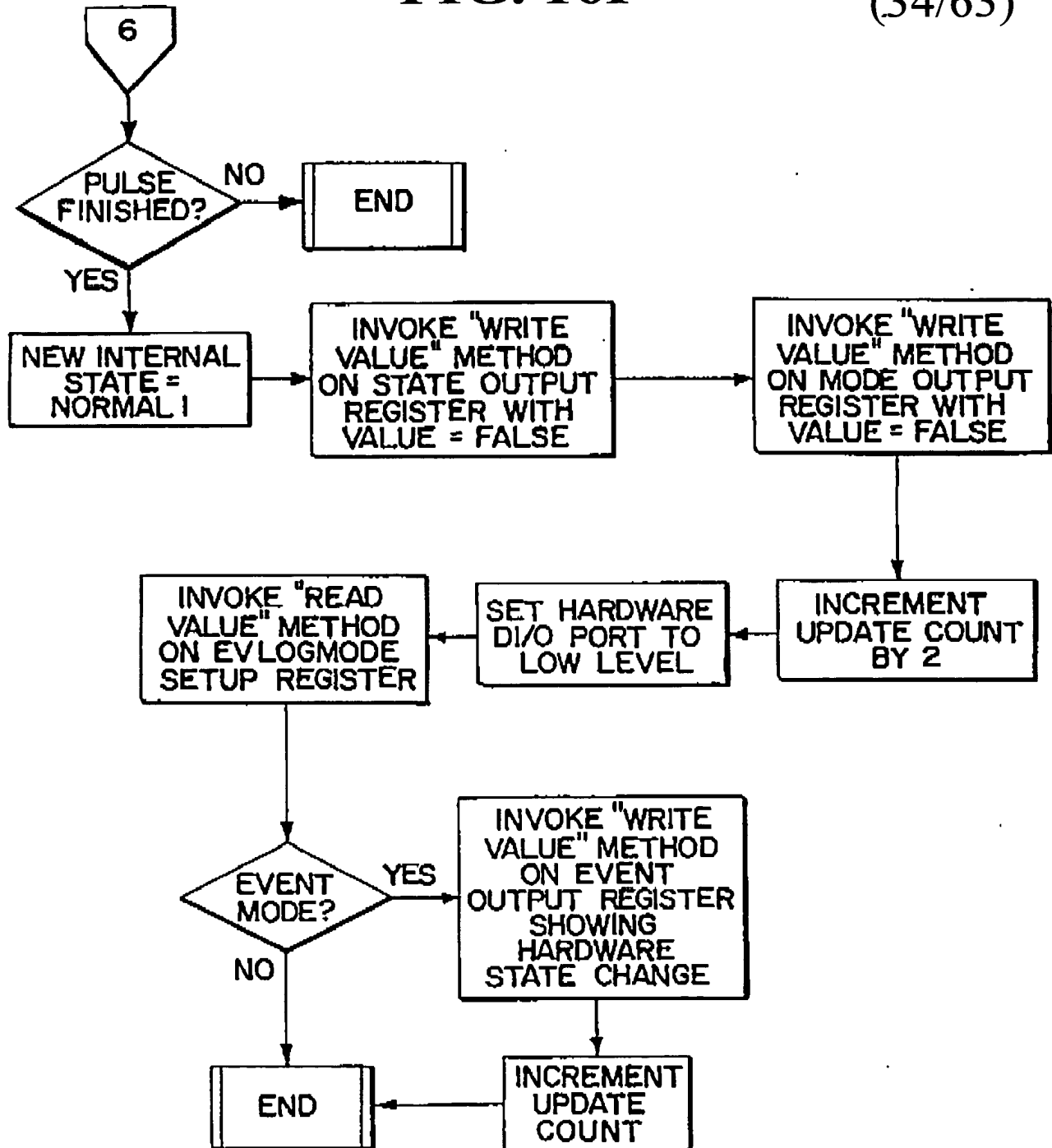


FIG. 16G
(35/63)

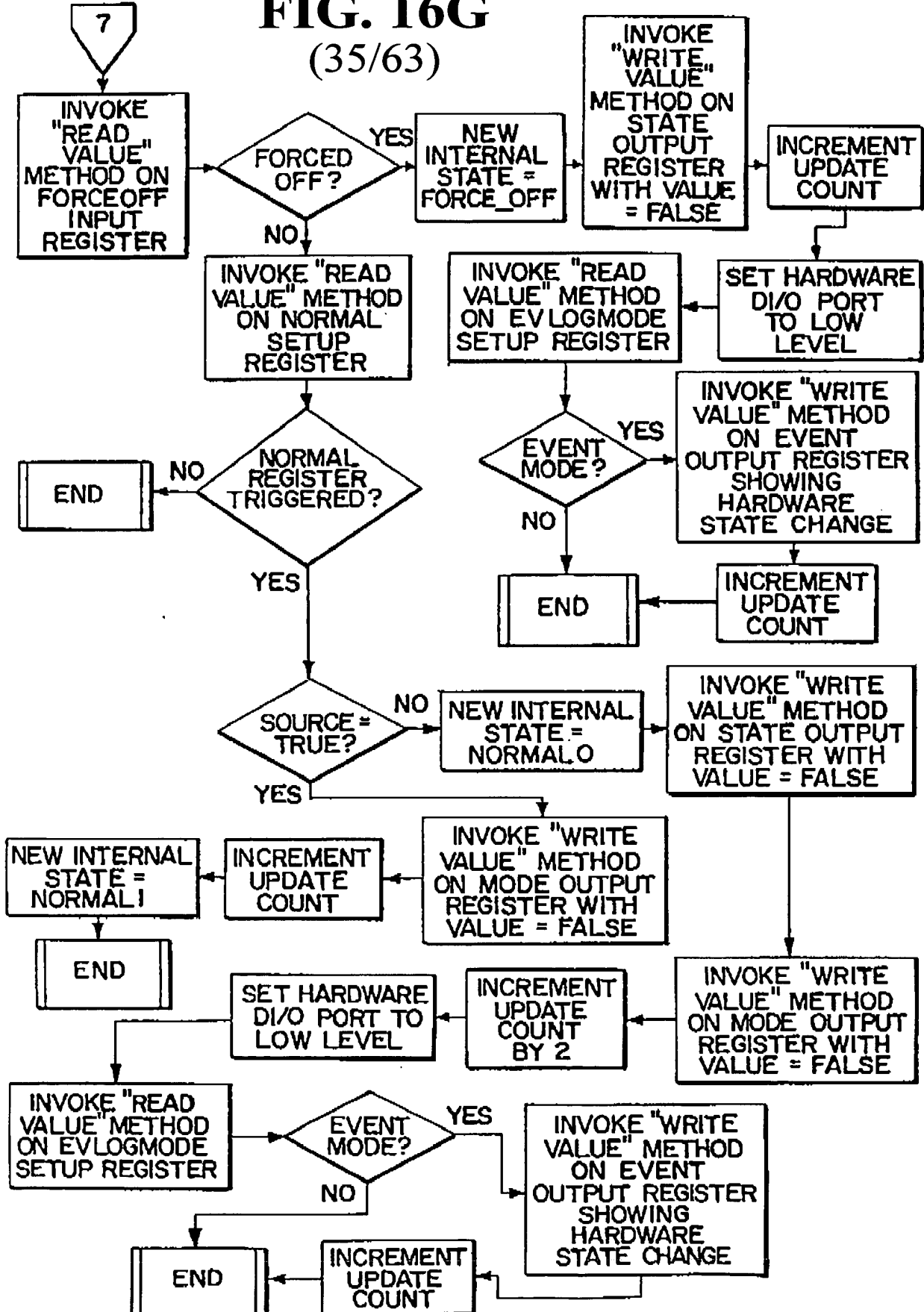


FIG. 16H
(36/63)

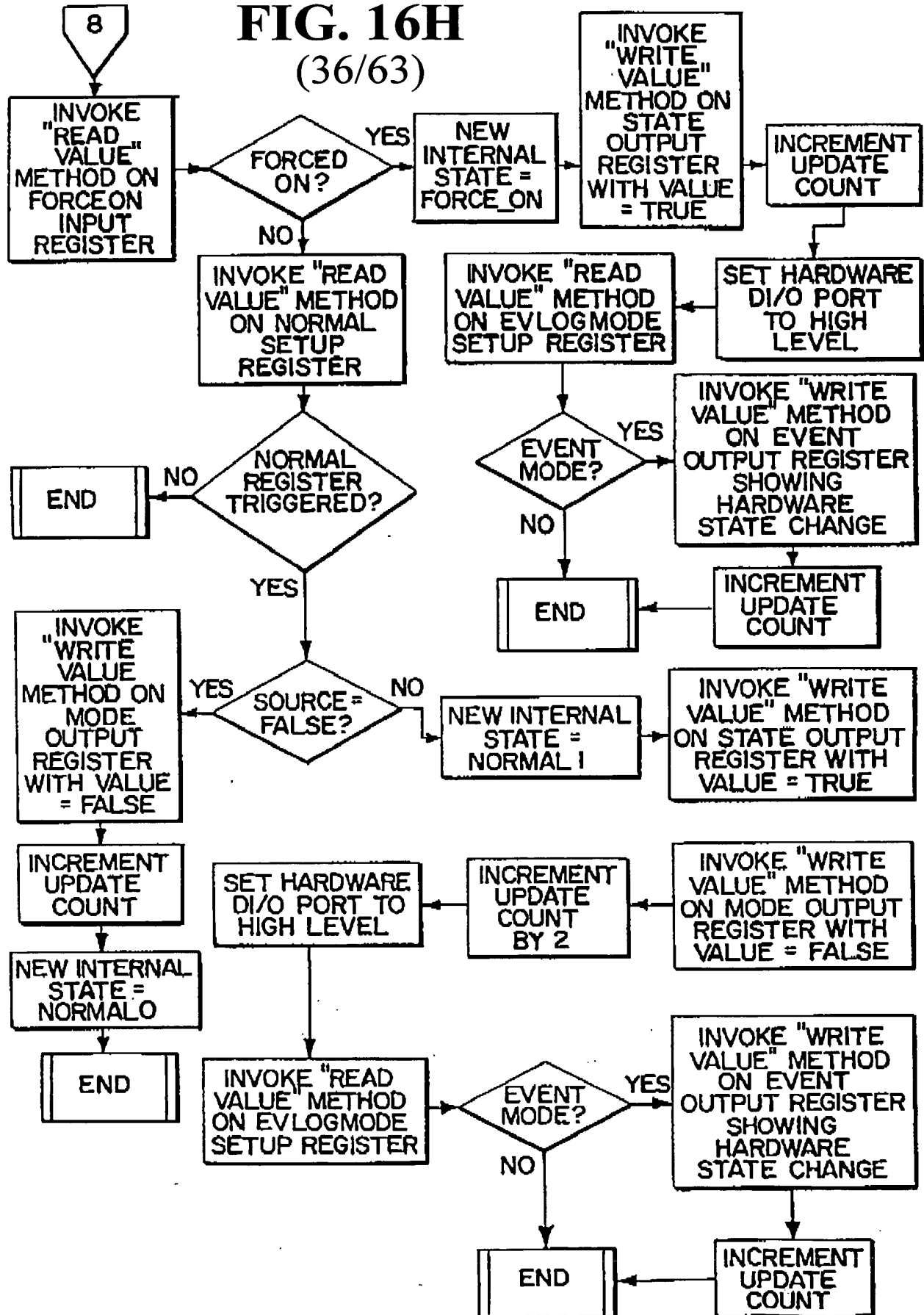


FIG. 17

(37/63)

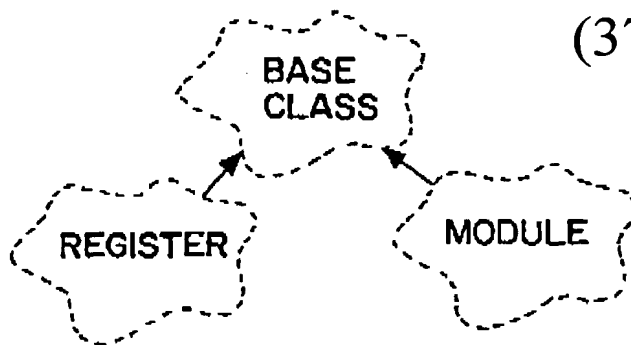


FIG. 17A

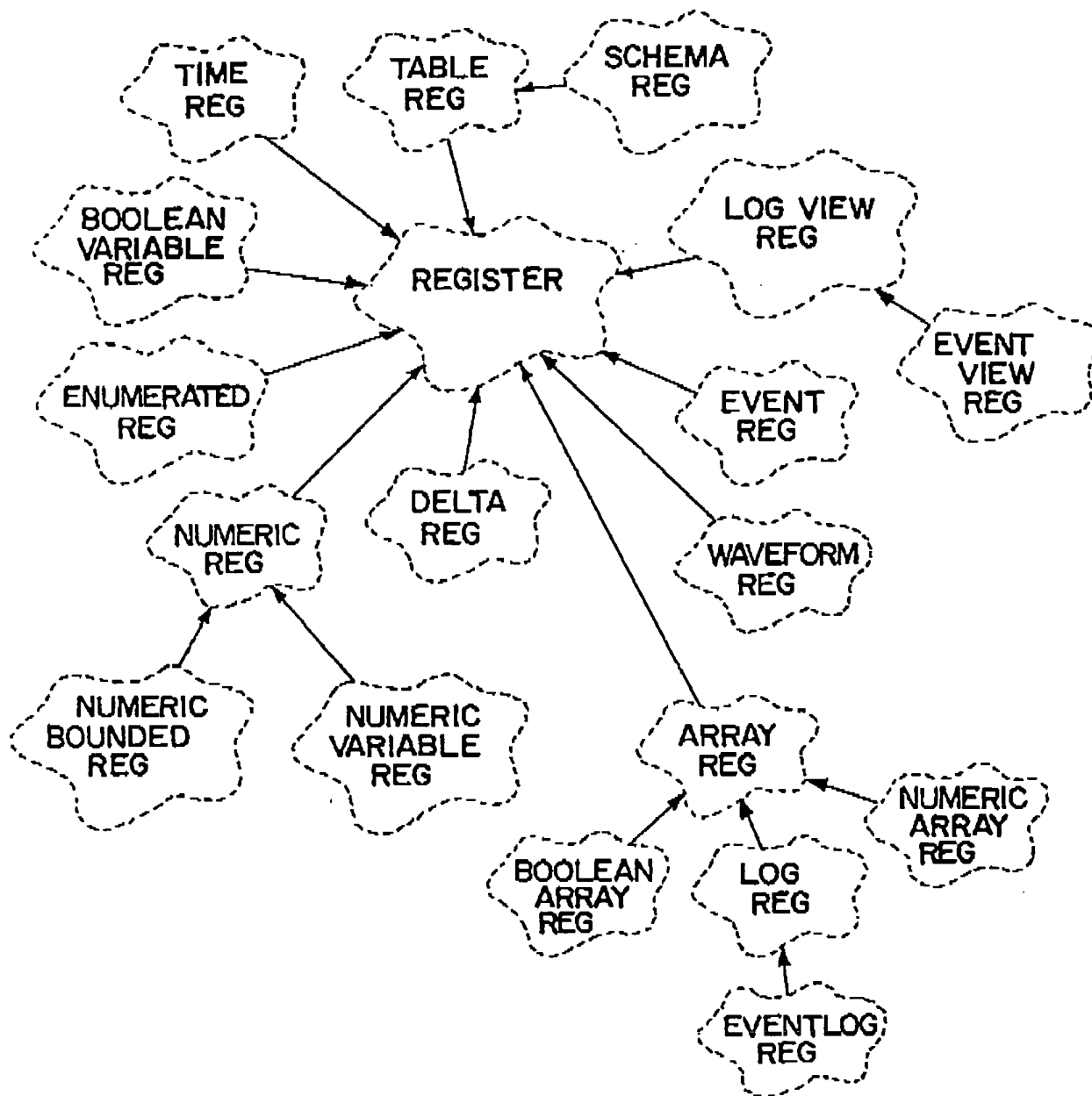


FIG. 17B

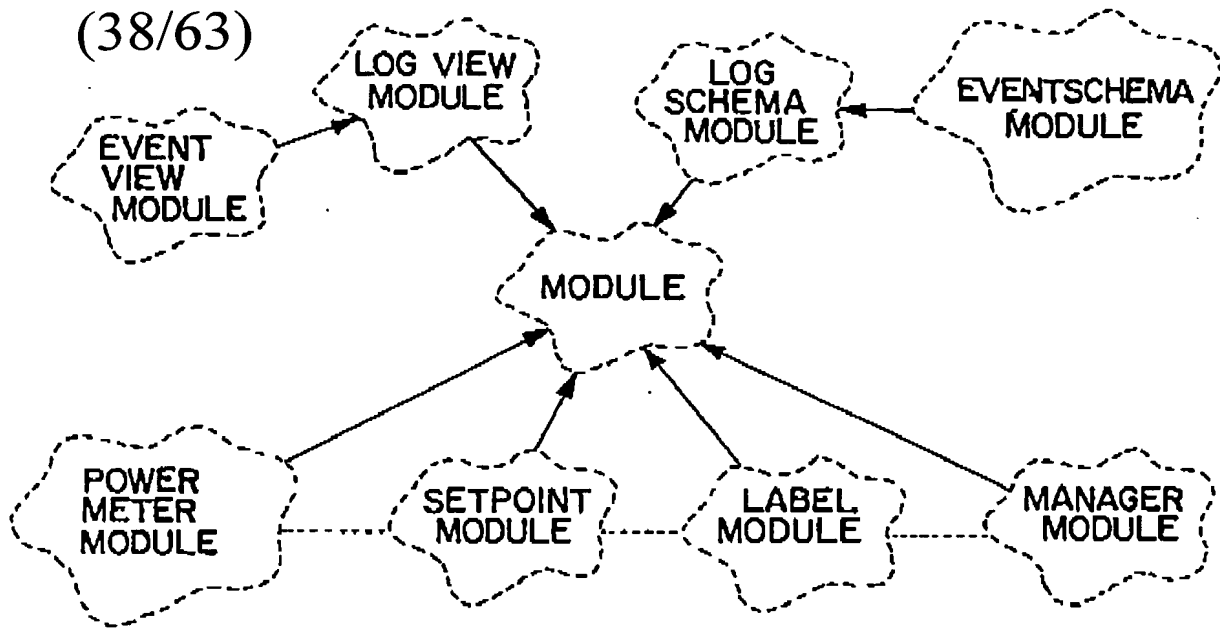


FIG. 17C

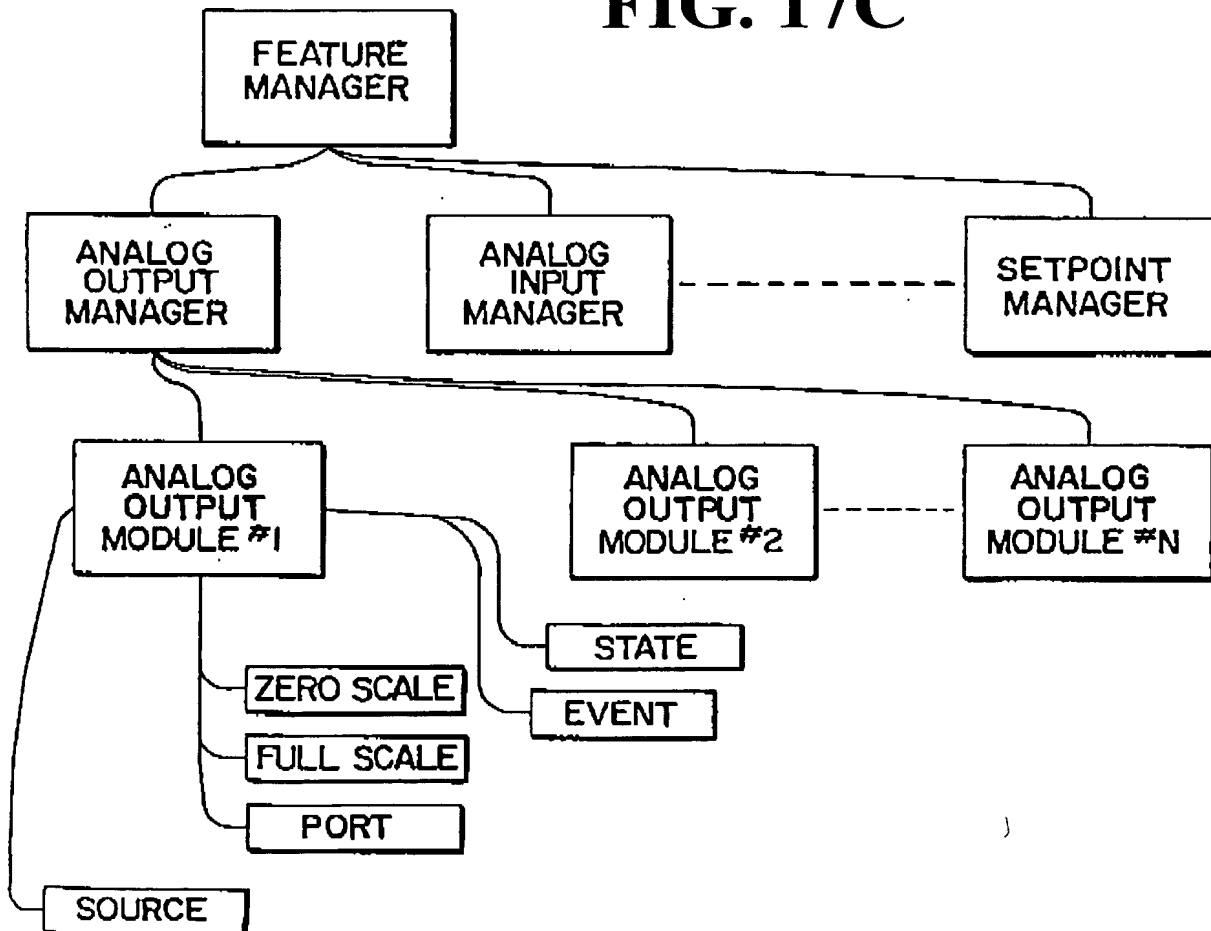


FIG. 18

(39/63)

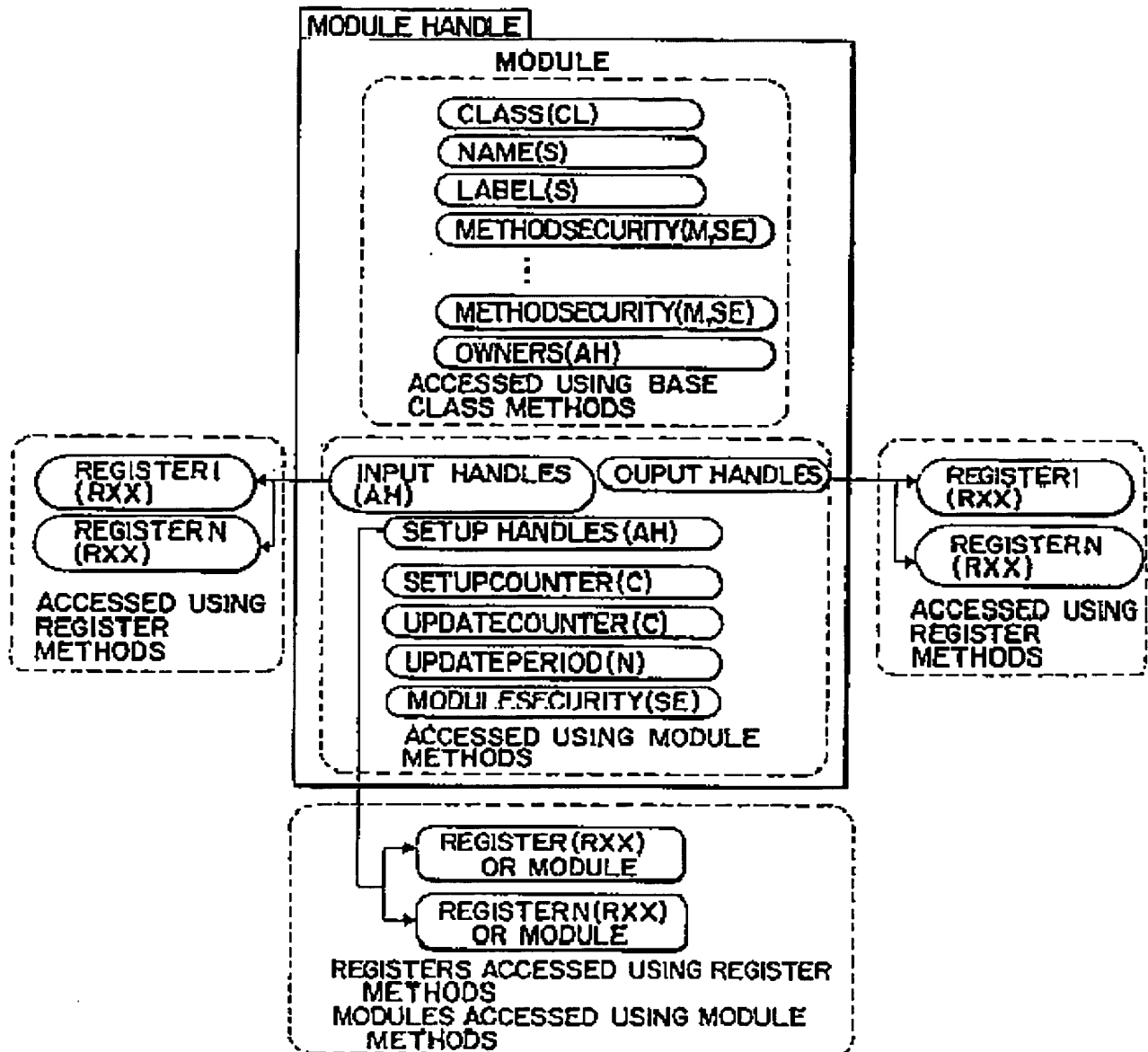


FIG. 19

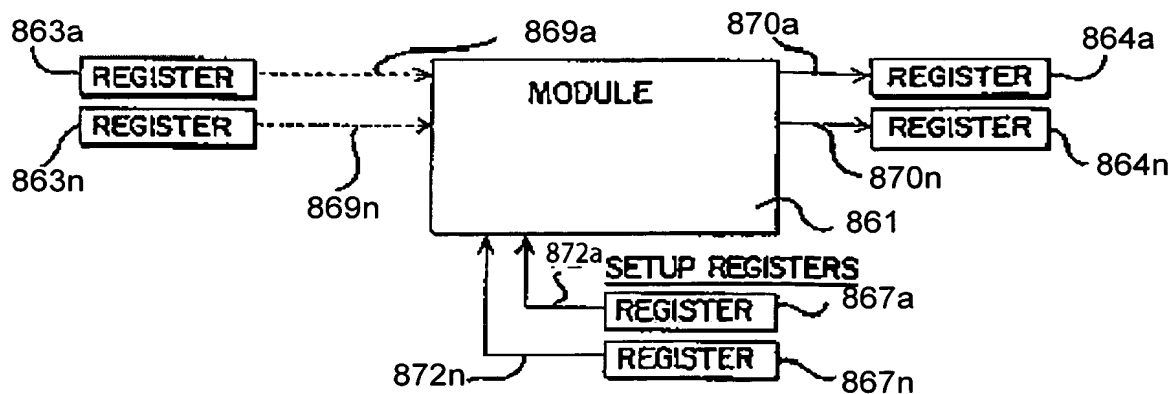


FIG. 19A
(40/63)

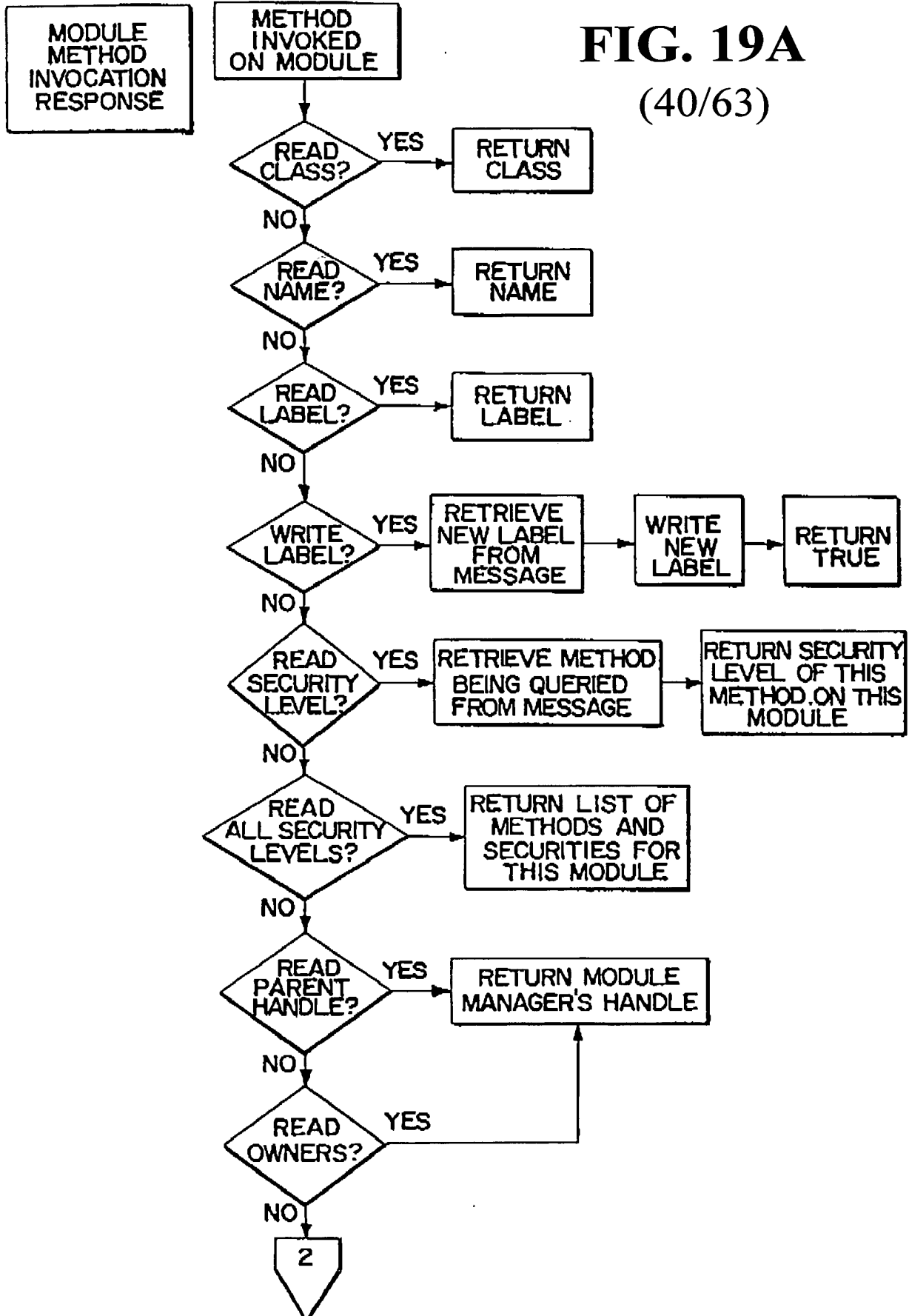
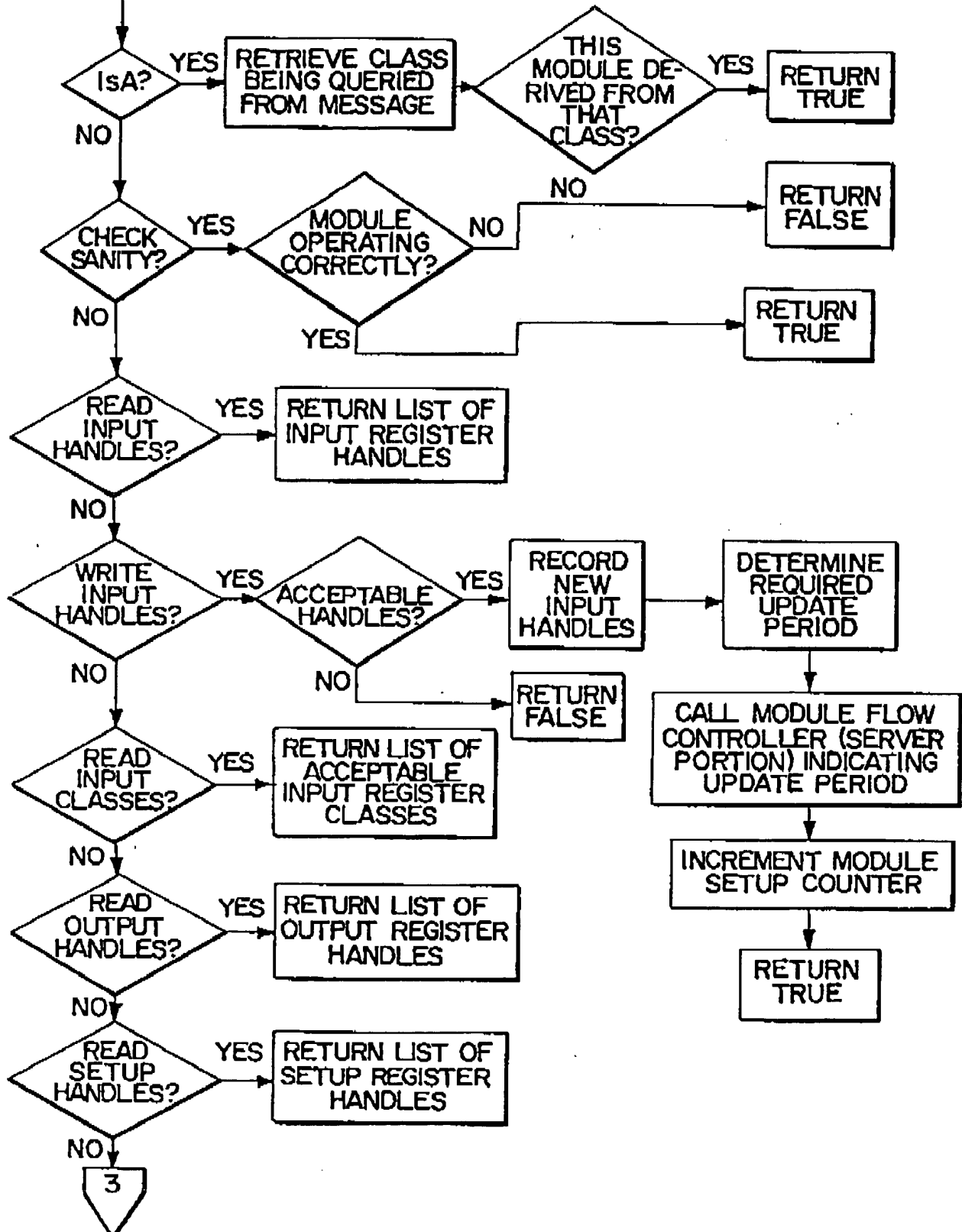


FIG. 19B

(41/63)



(42/63)

FIG. 19C

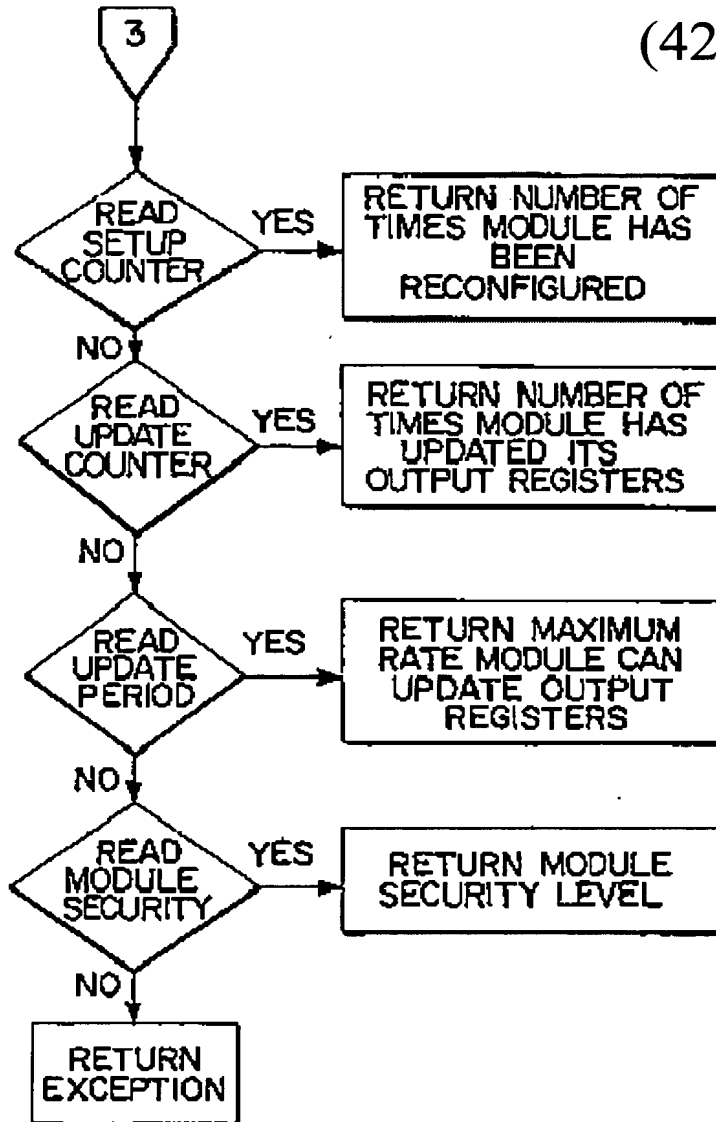


FIG. 20

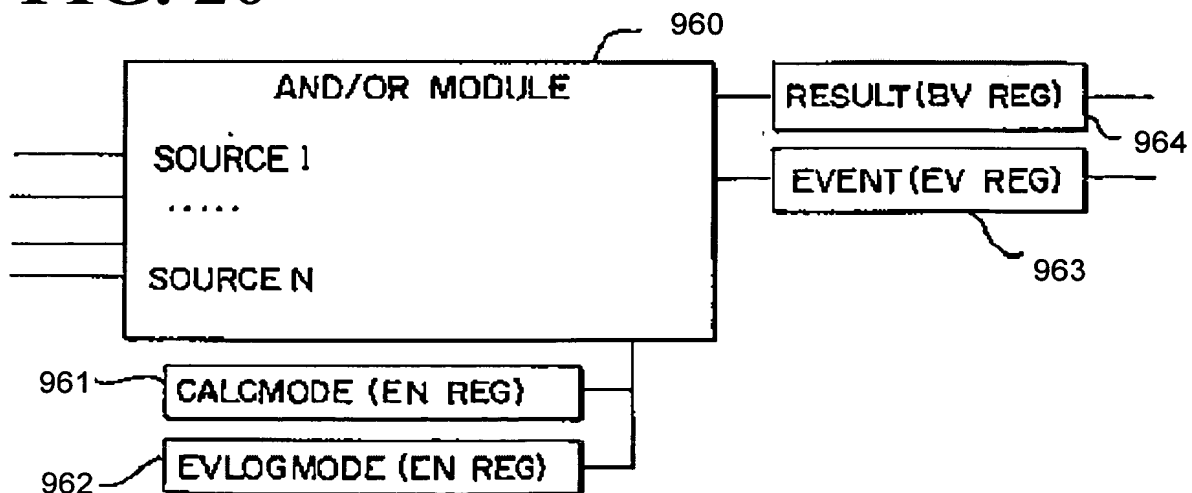


FIG. 20A

(43/63)

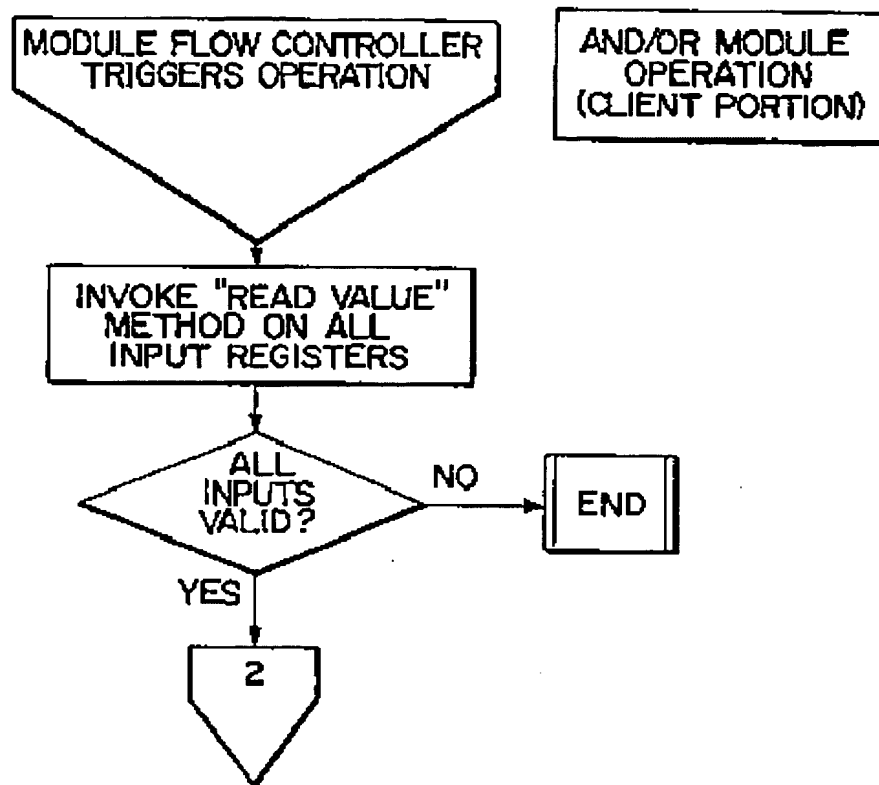


FIG. 21

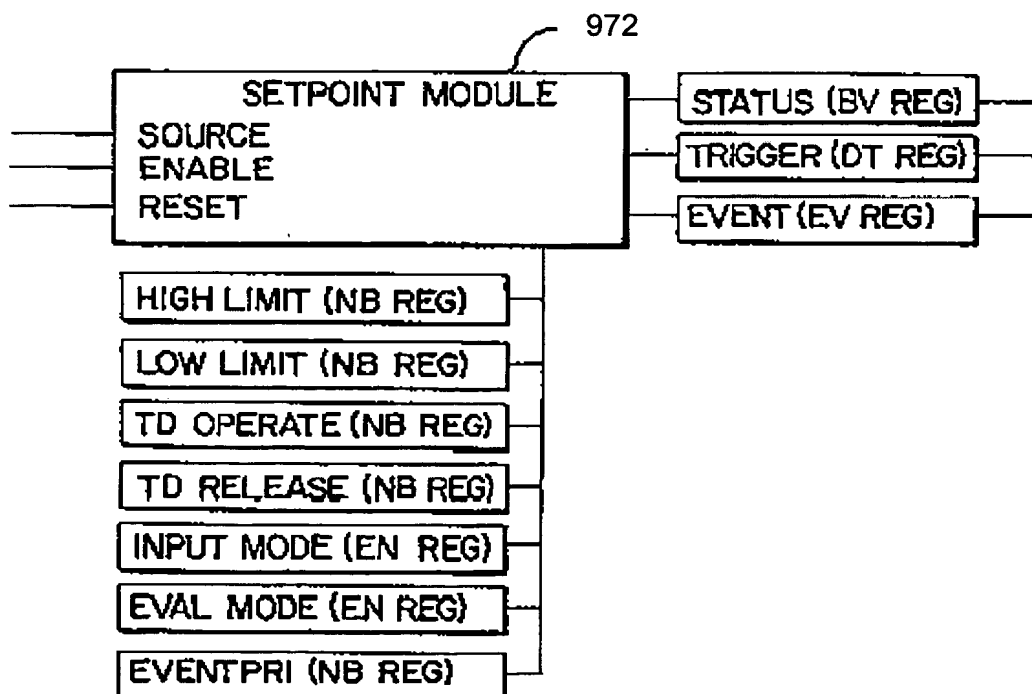


FIG. 20B

(44/63)

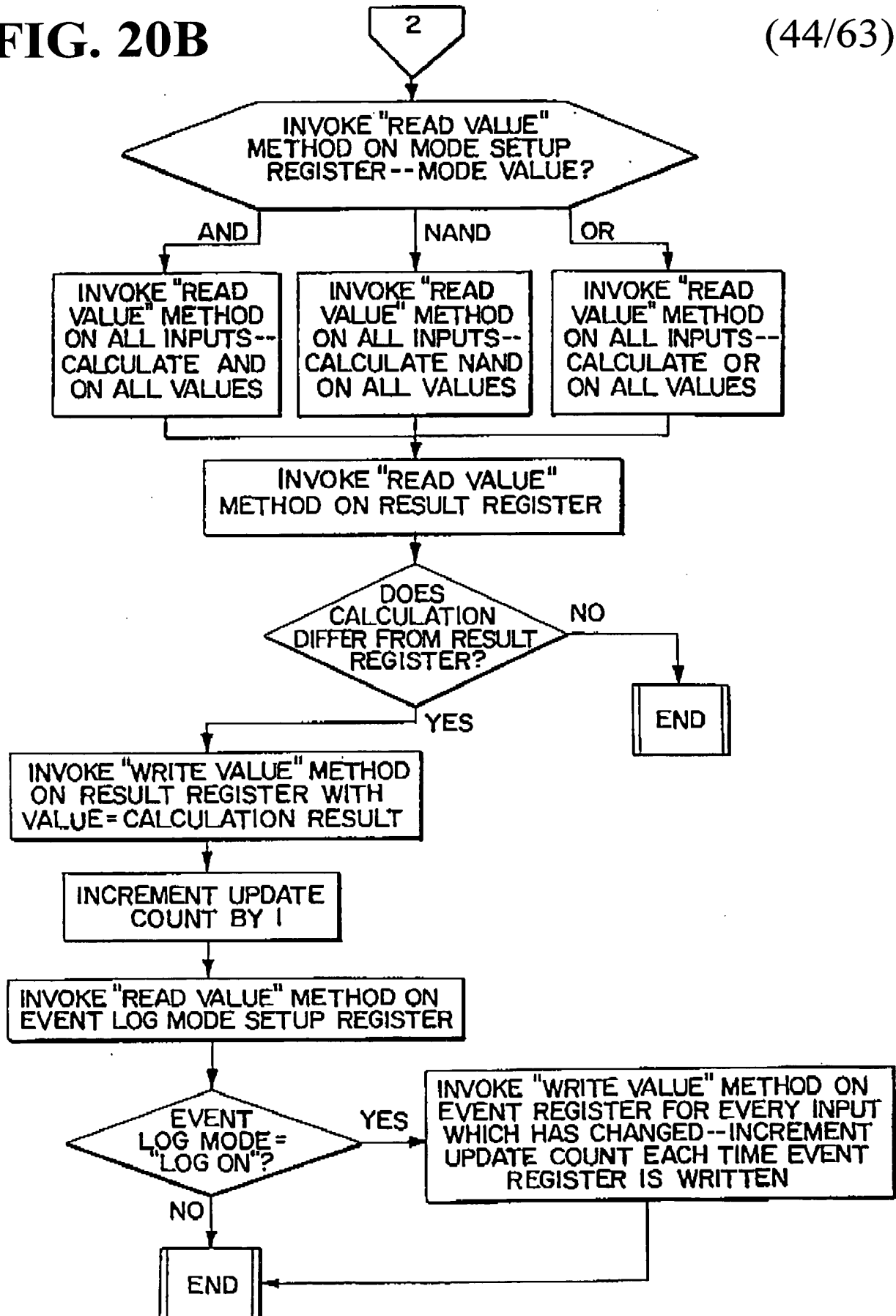


FIG. 21A

(45/63)

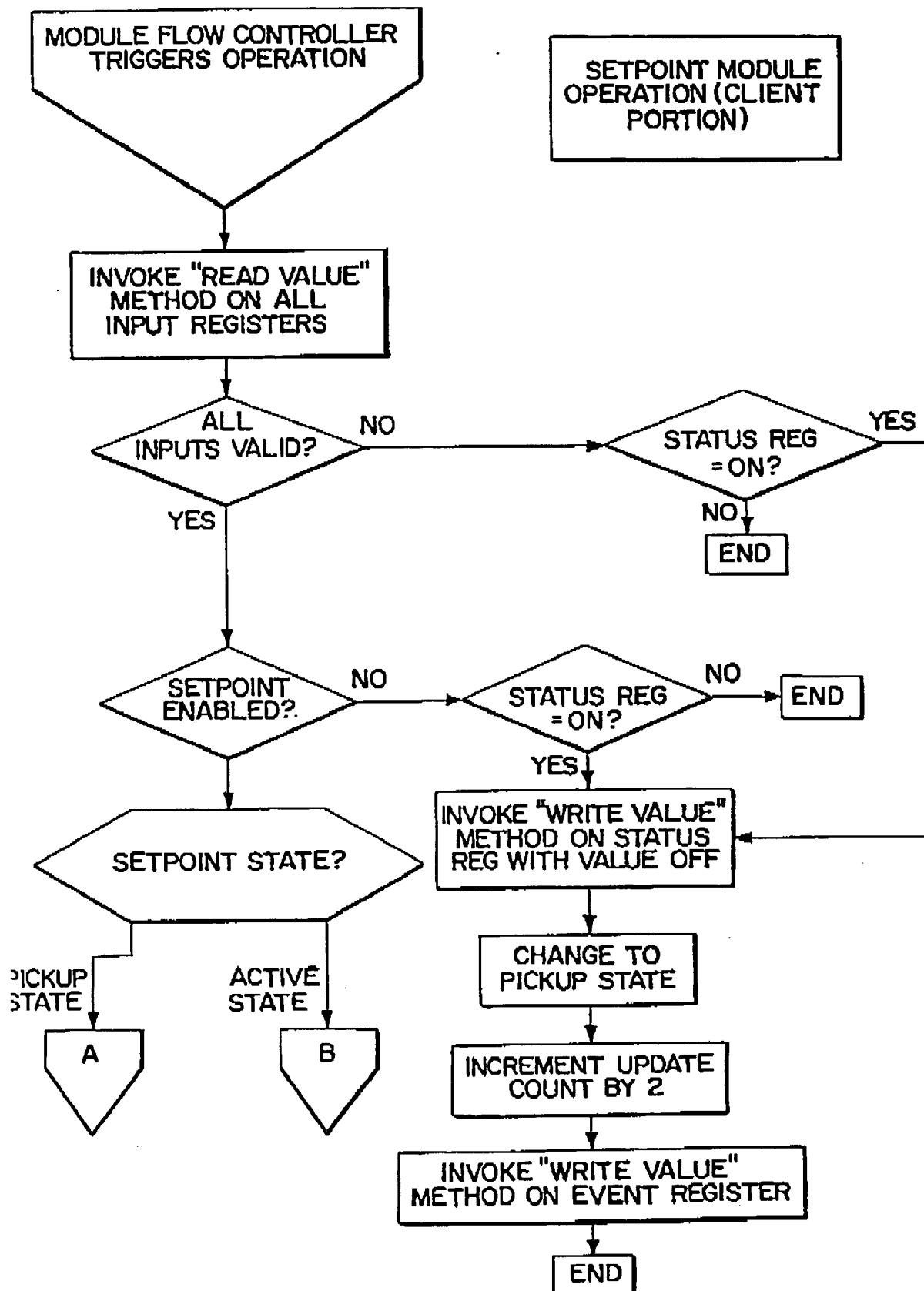


FIG. 21B

(46/63)

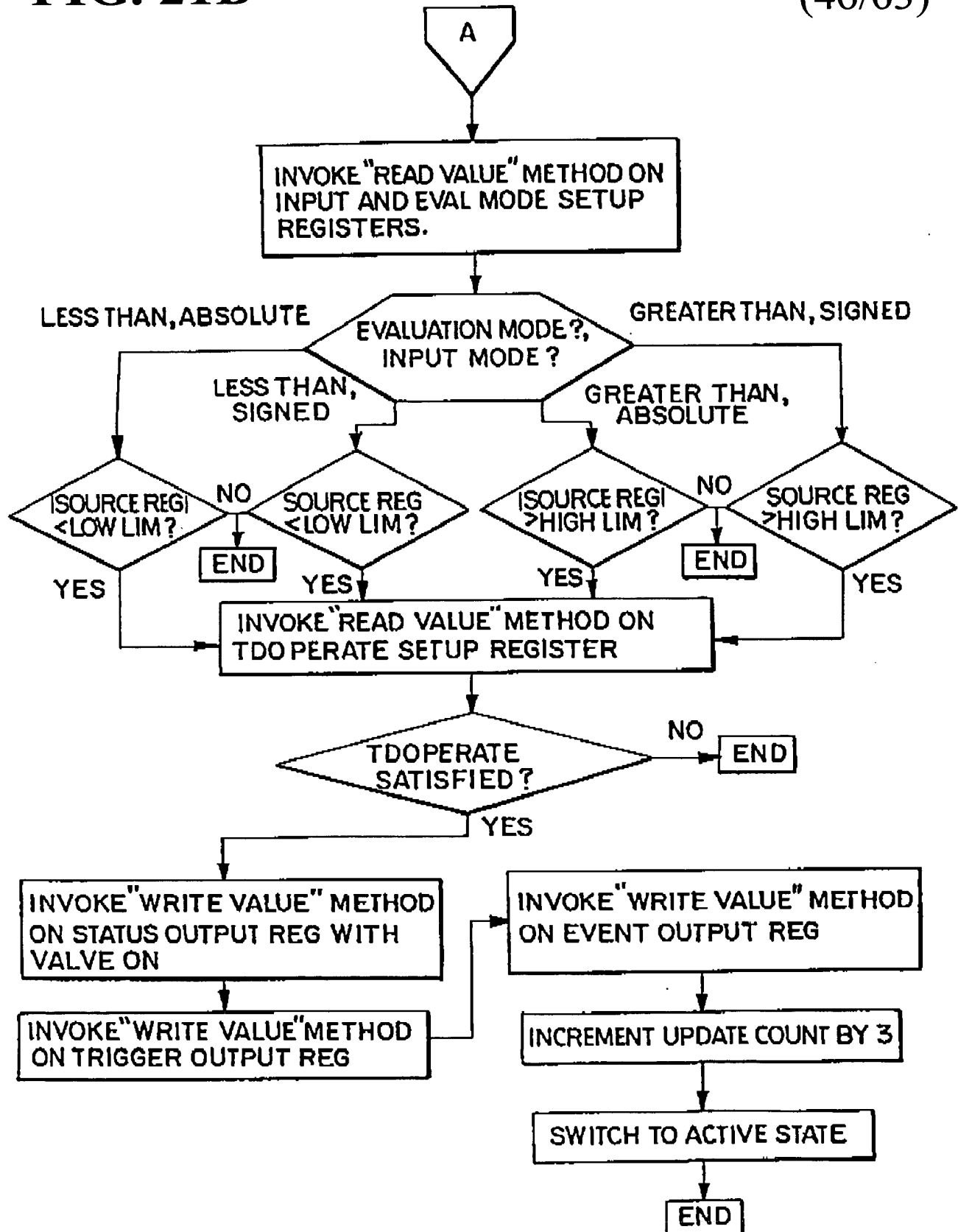
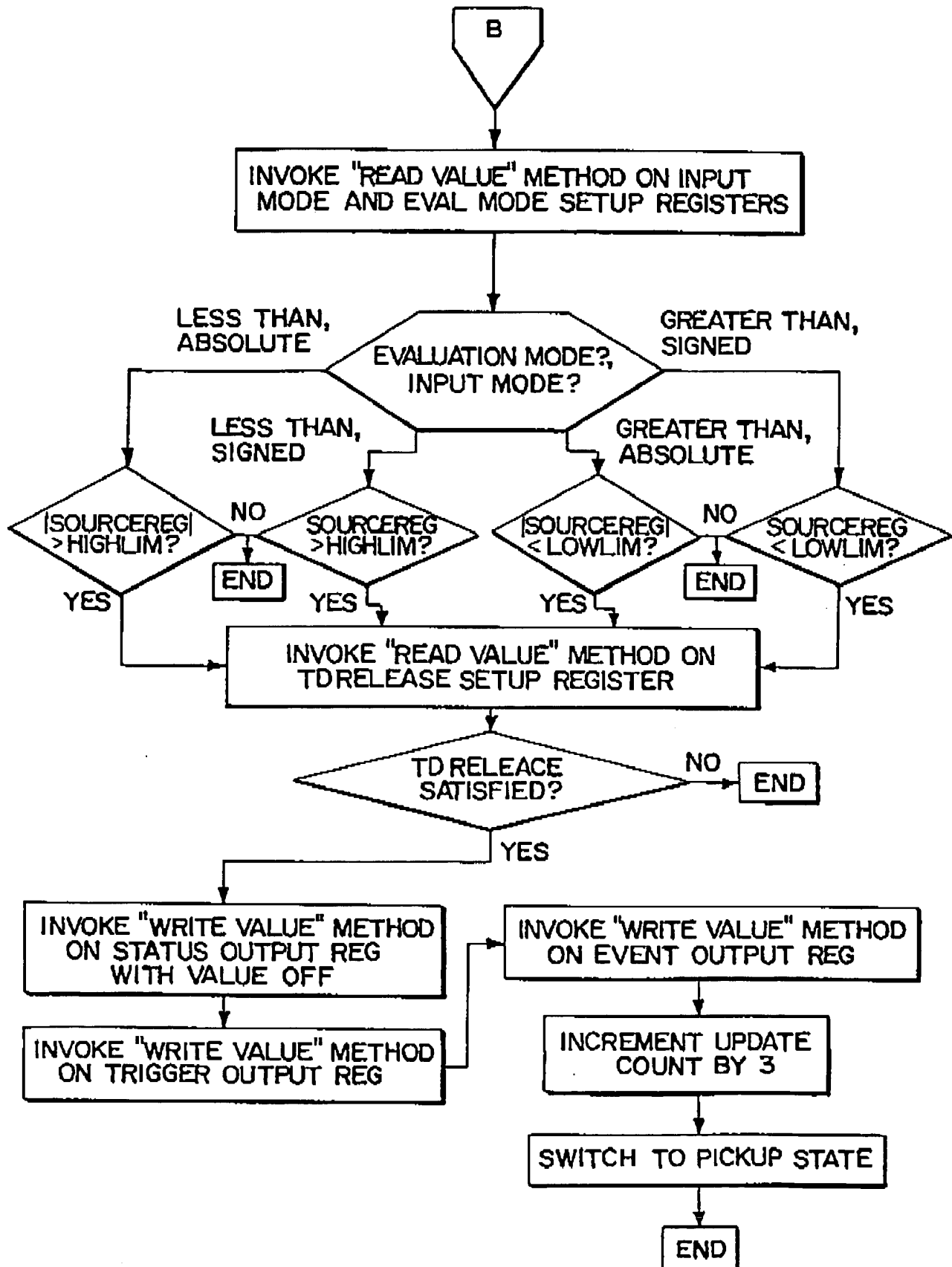


FIG. 21C

(47/63)



(48/63)

FIG. 22A

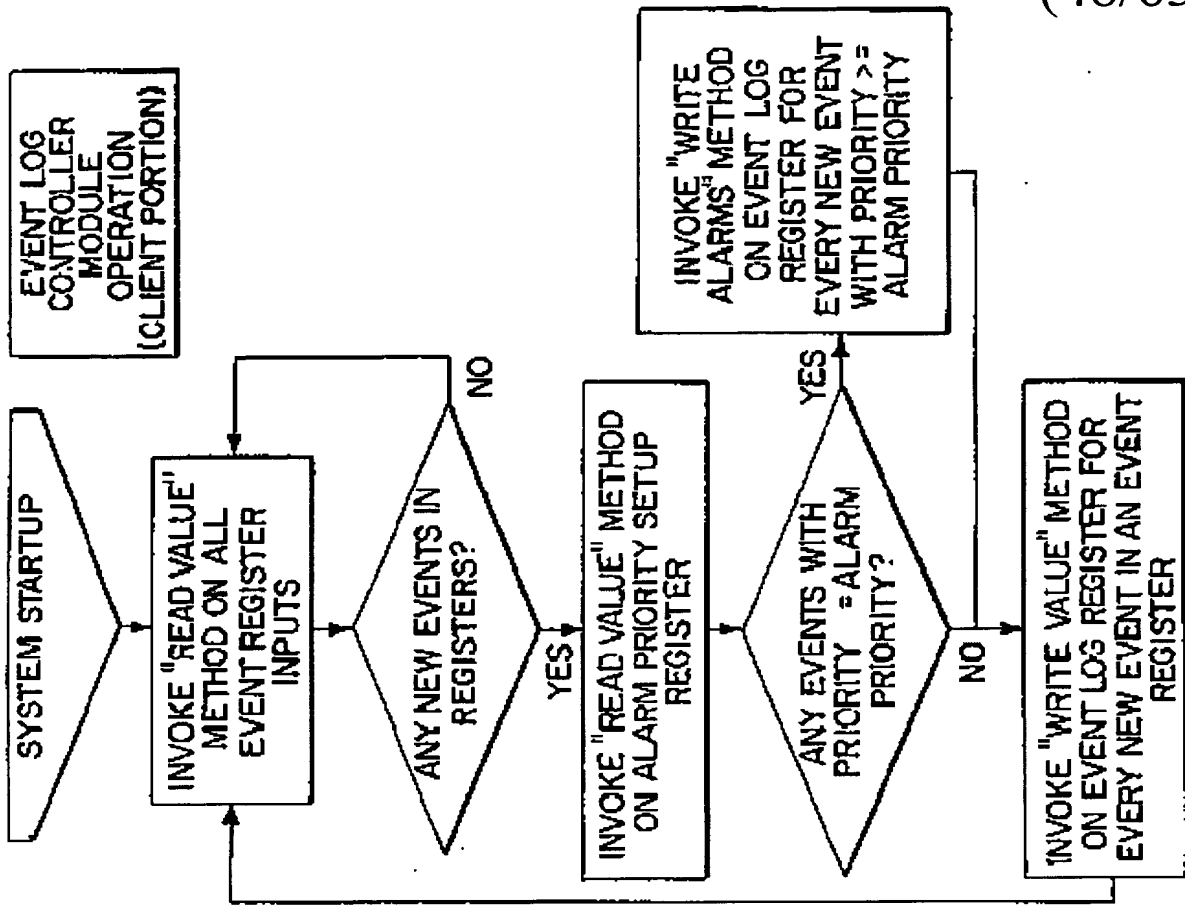


FIG. 22

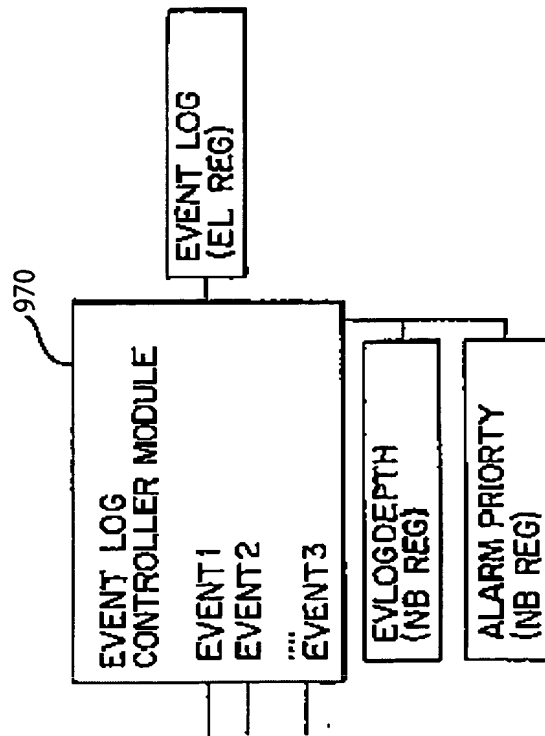


FIG. 23

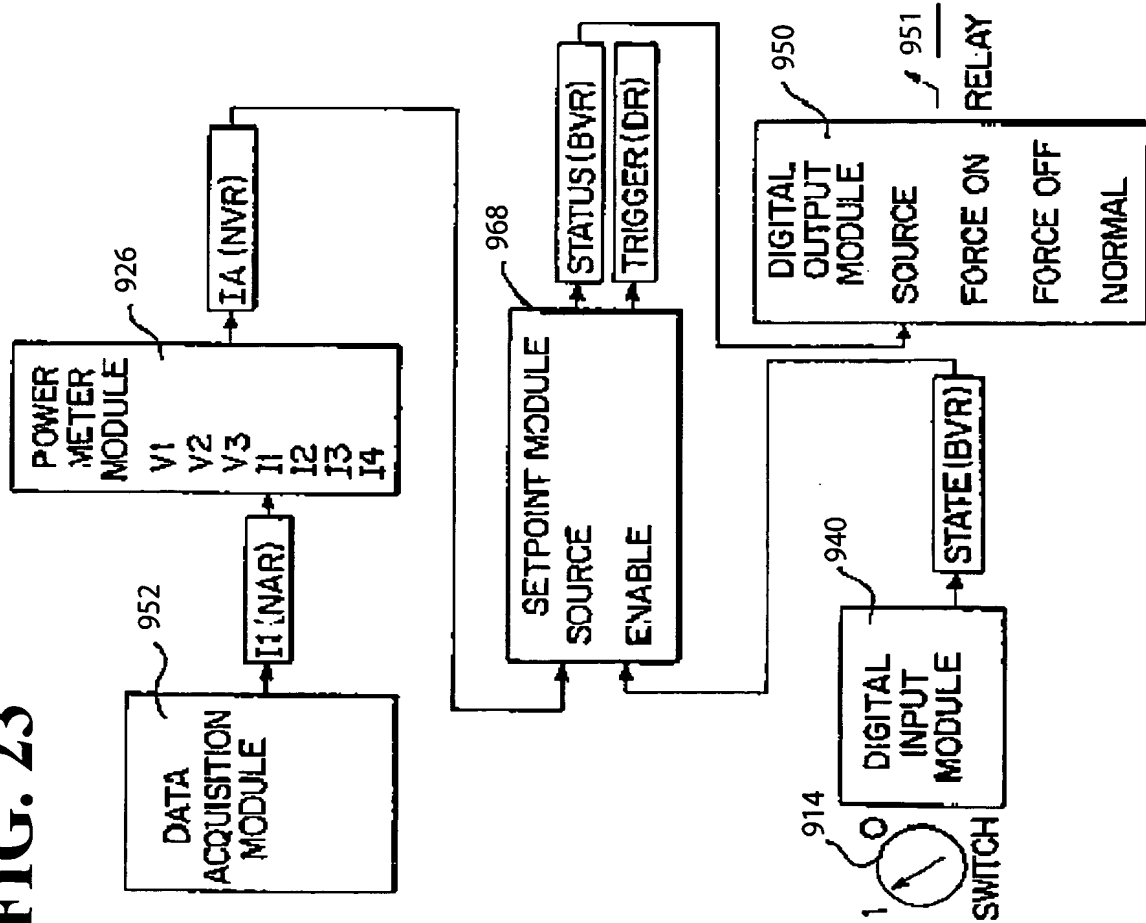
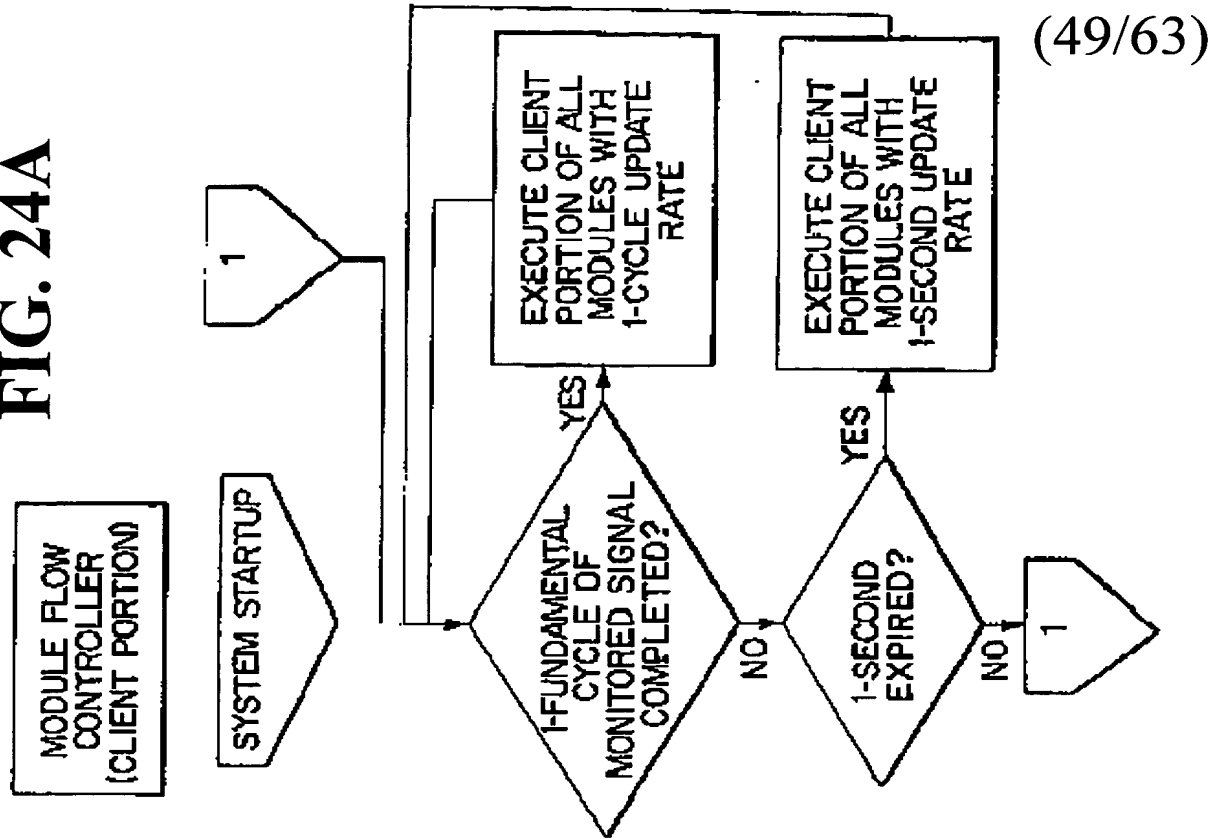


FIG. 24A



(49/63)

(50/63)

FIG. 25

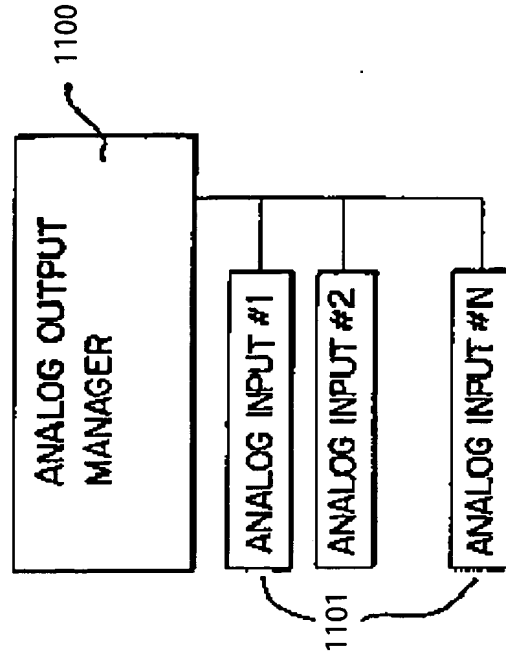


FIG. 24B

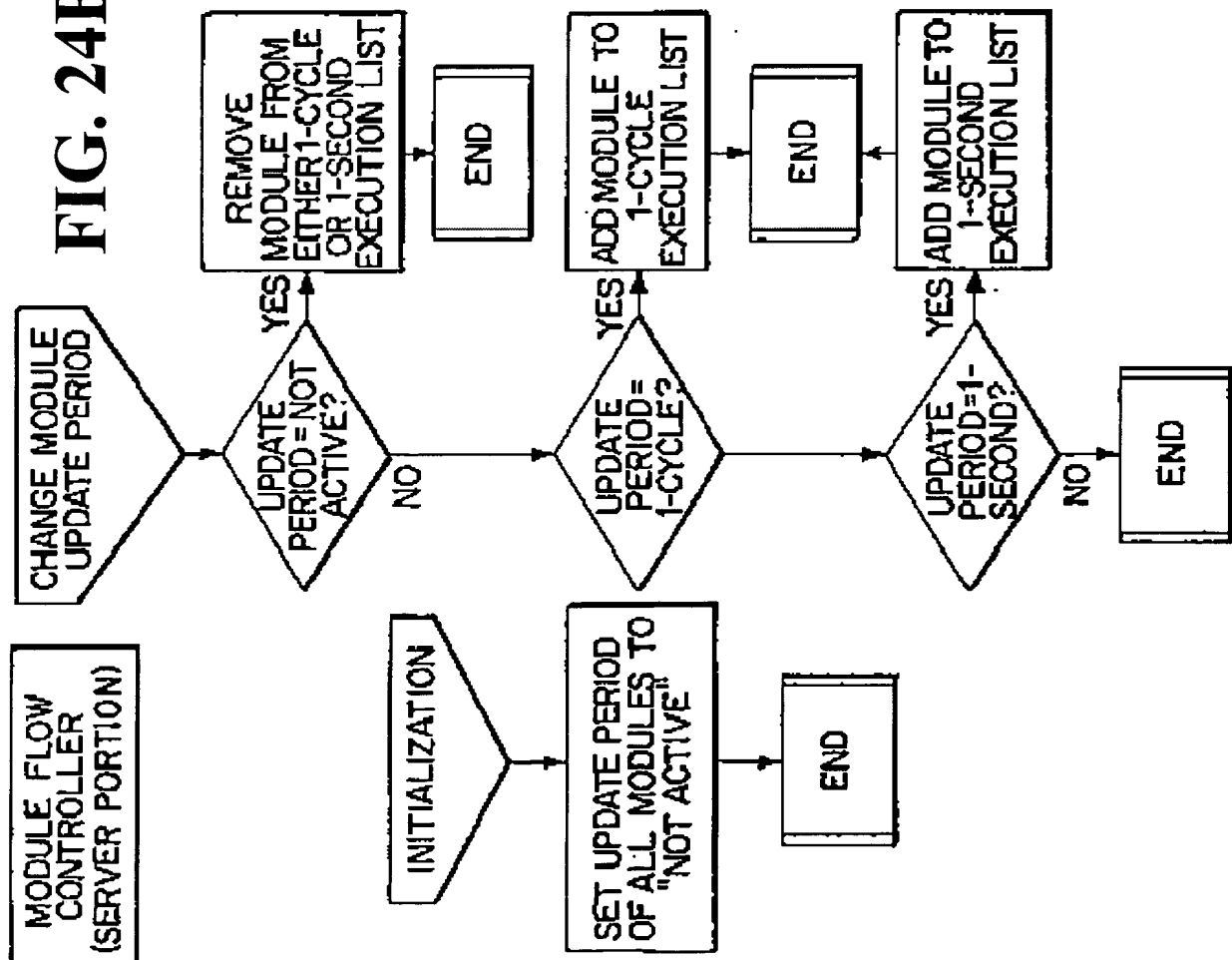


FIG. 25A

(51/63)

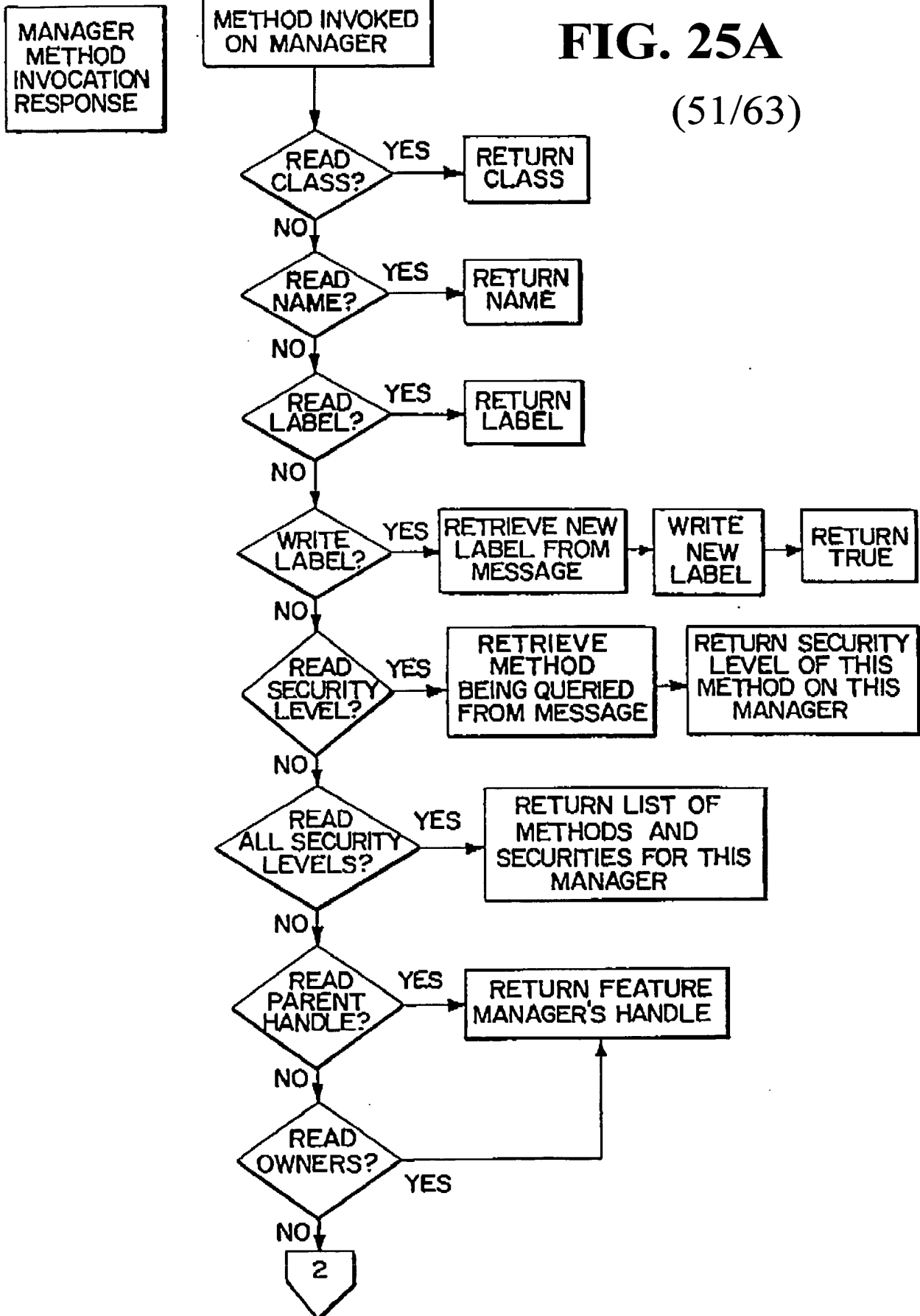


FIG. 25B

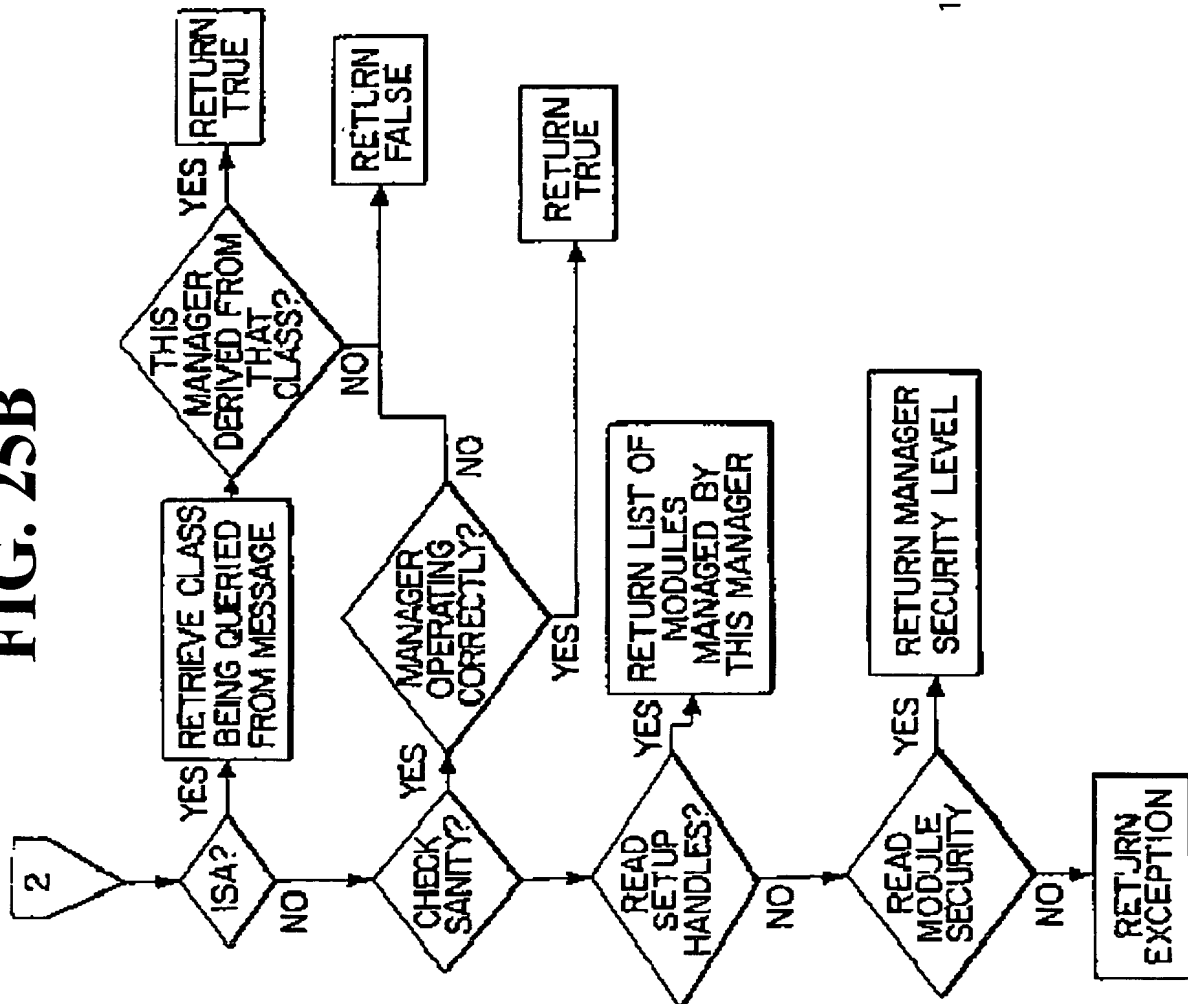
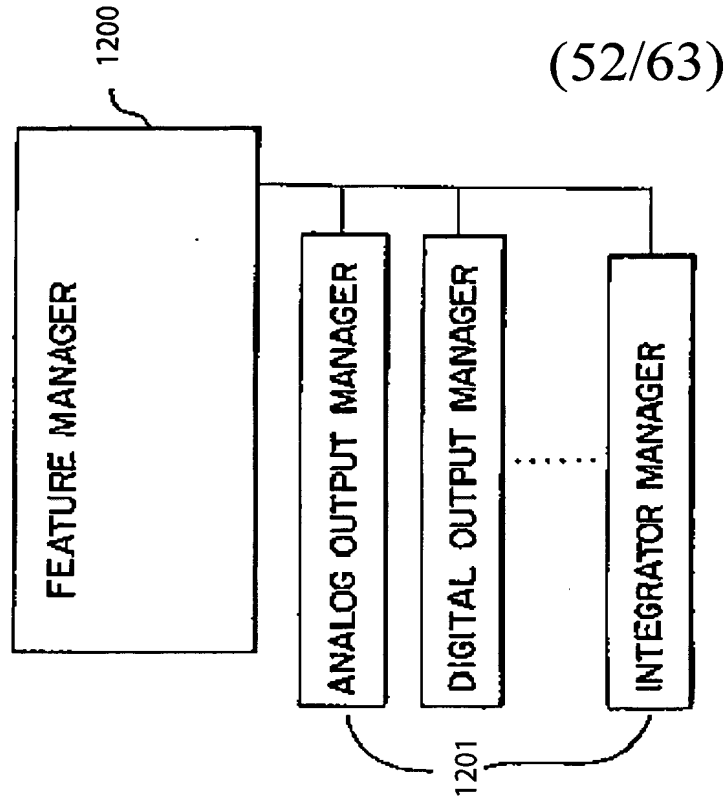


FIG. 26



(52/63)

FEATURE
MANAGER
METHOD
INVOCATION
RESPONSE

FIG. 26A

(53/63)

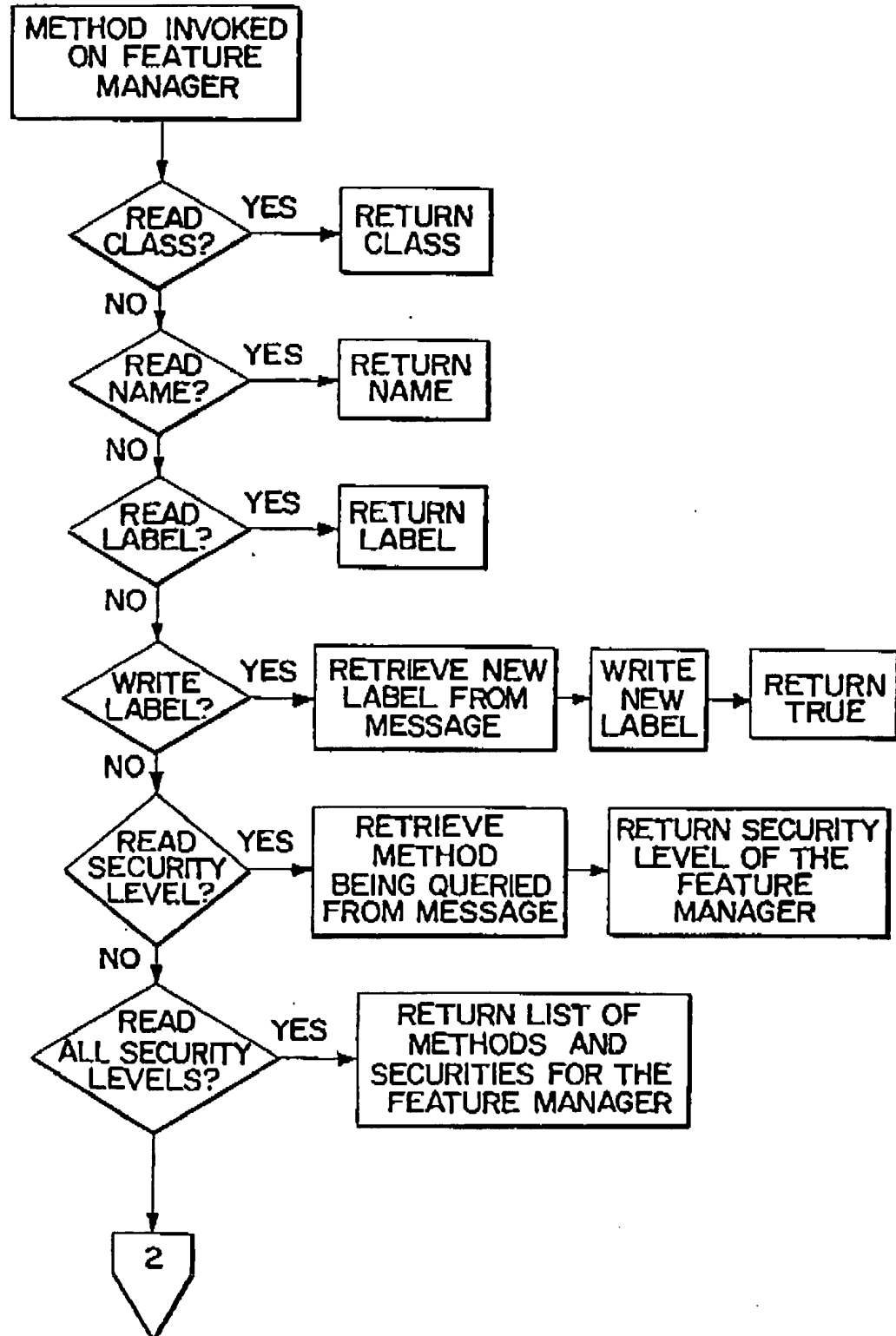
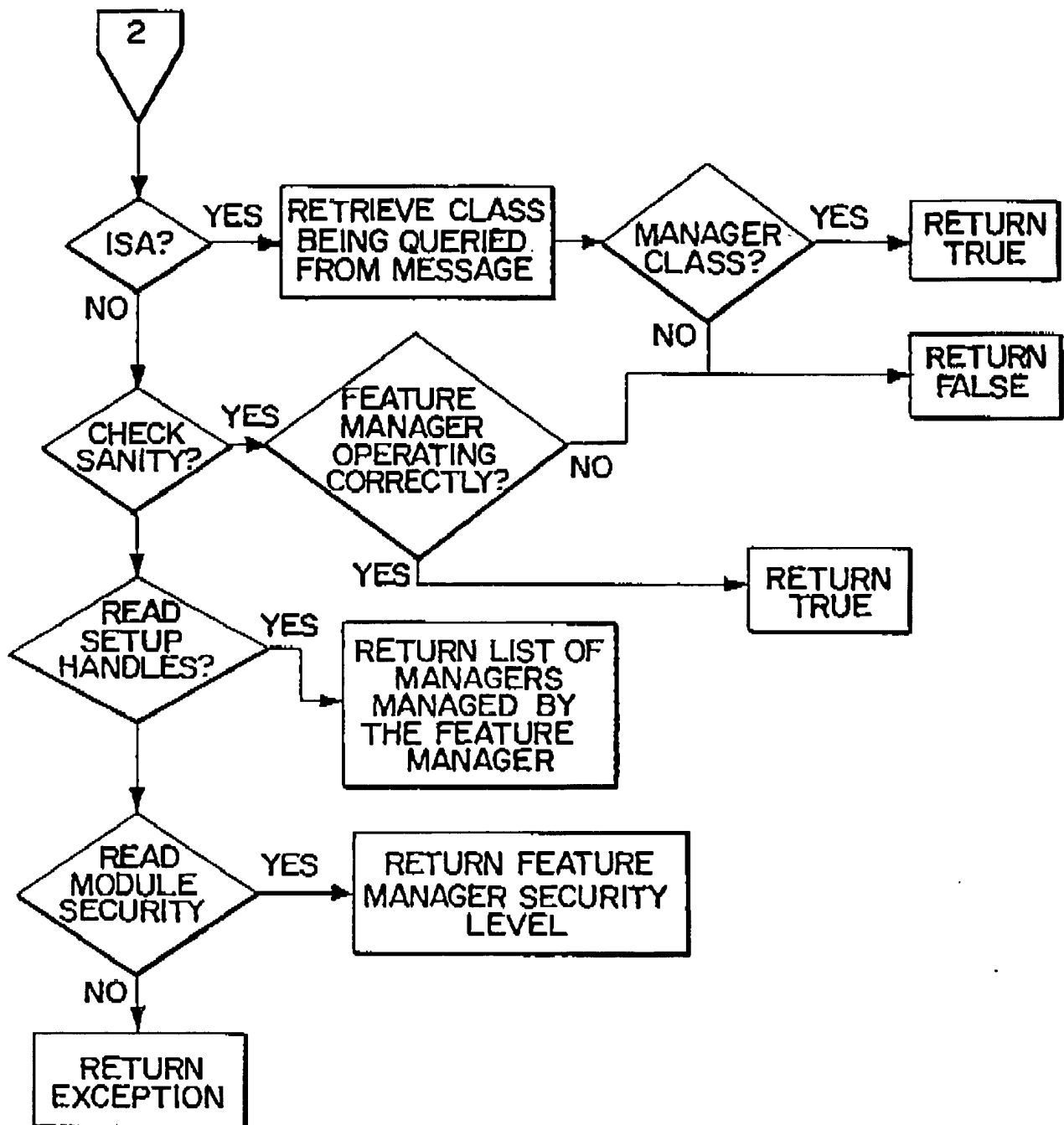


FIG. 26B

(54/63)



(55/63)

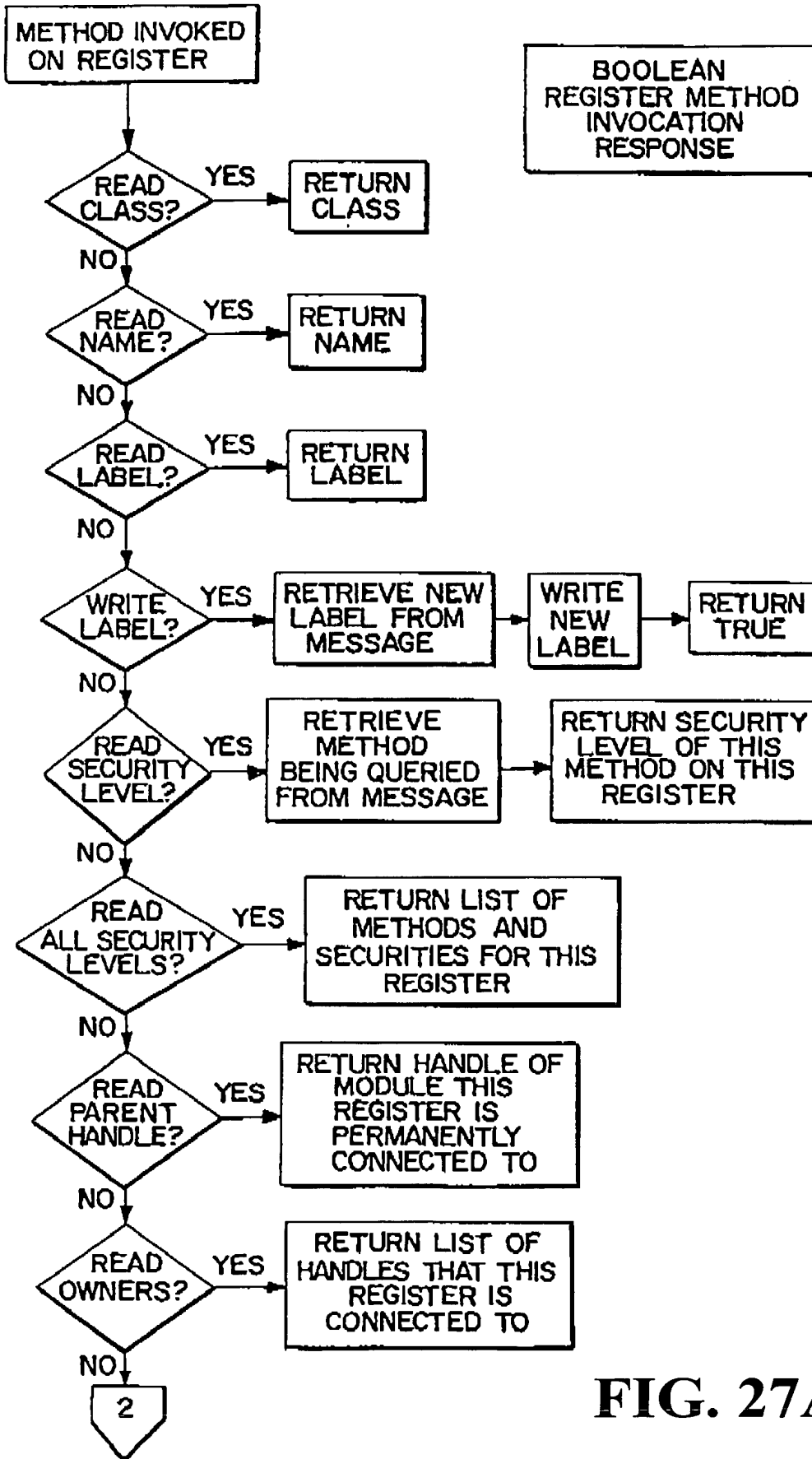


FIG. 27B (56/63)

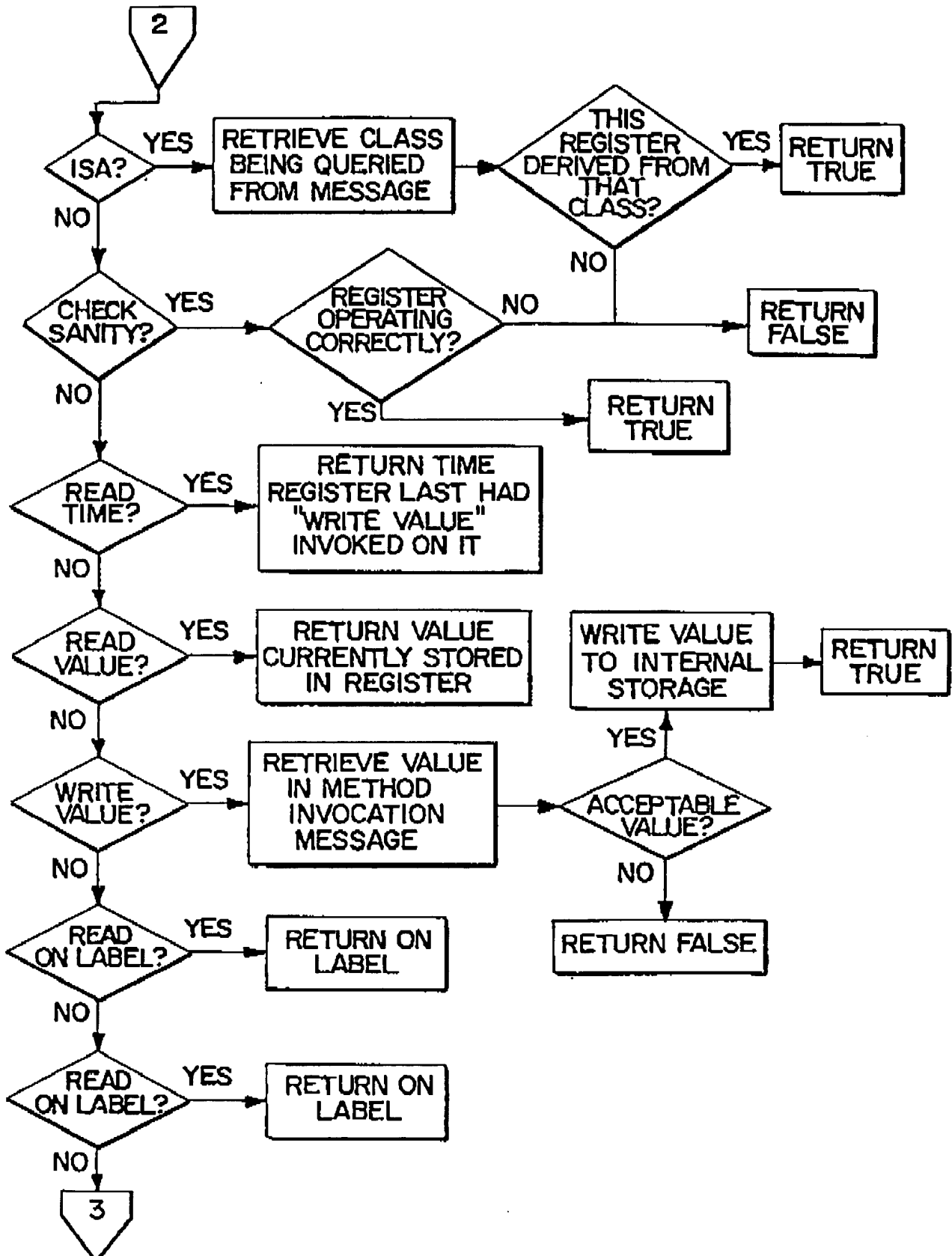


FIG. 27C

(57/63)

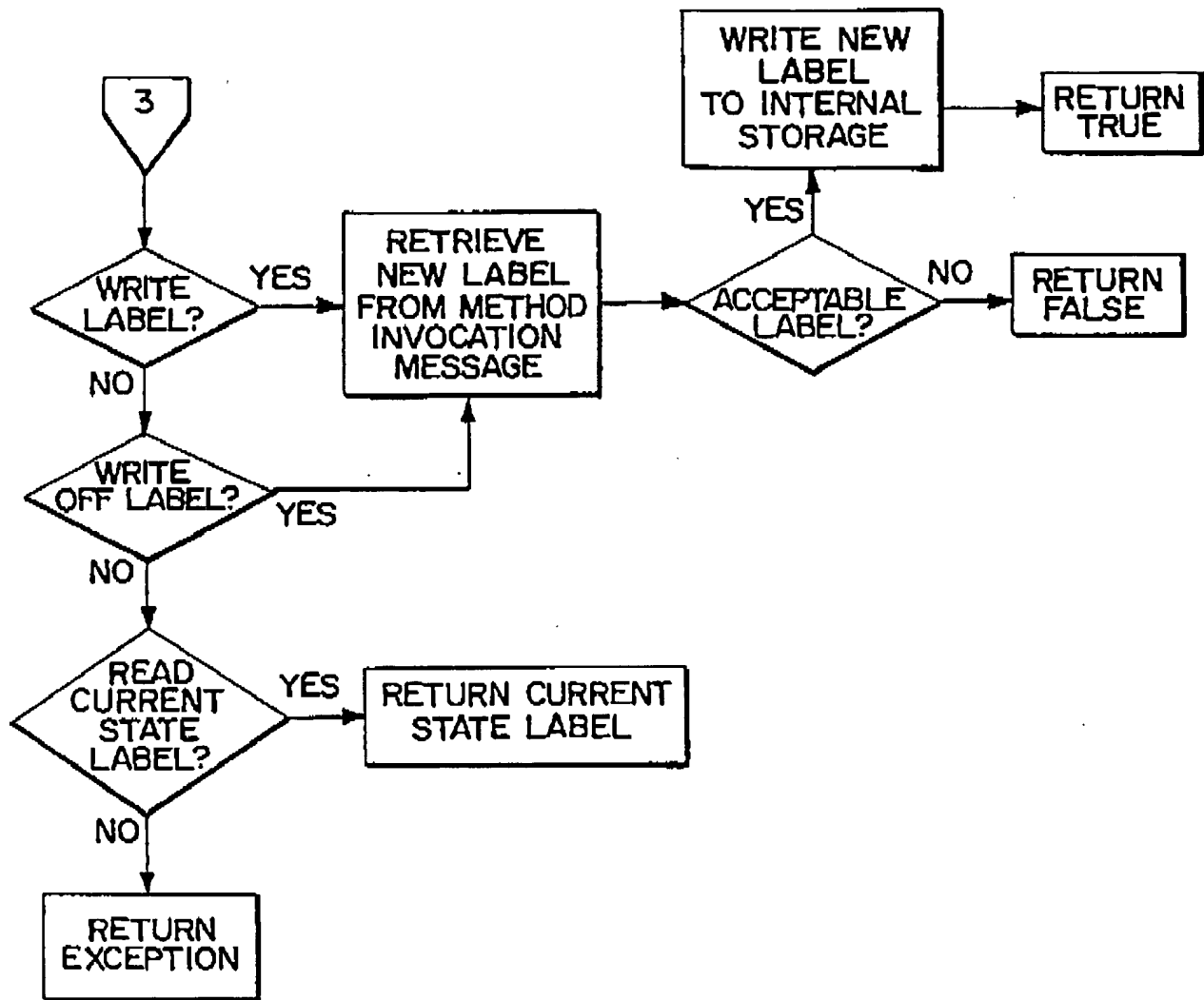


FIG. 28A

(58/63)

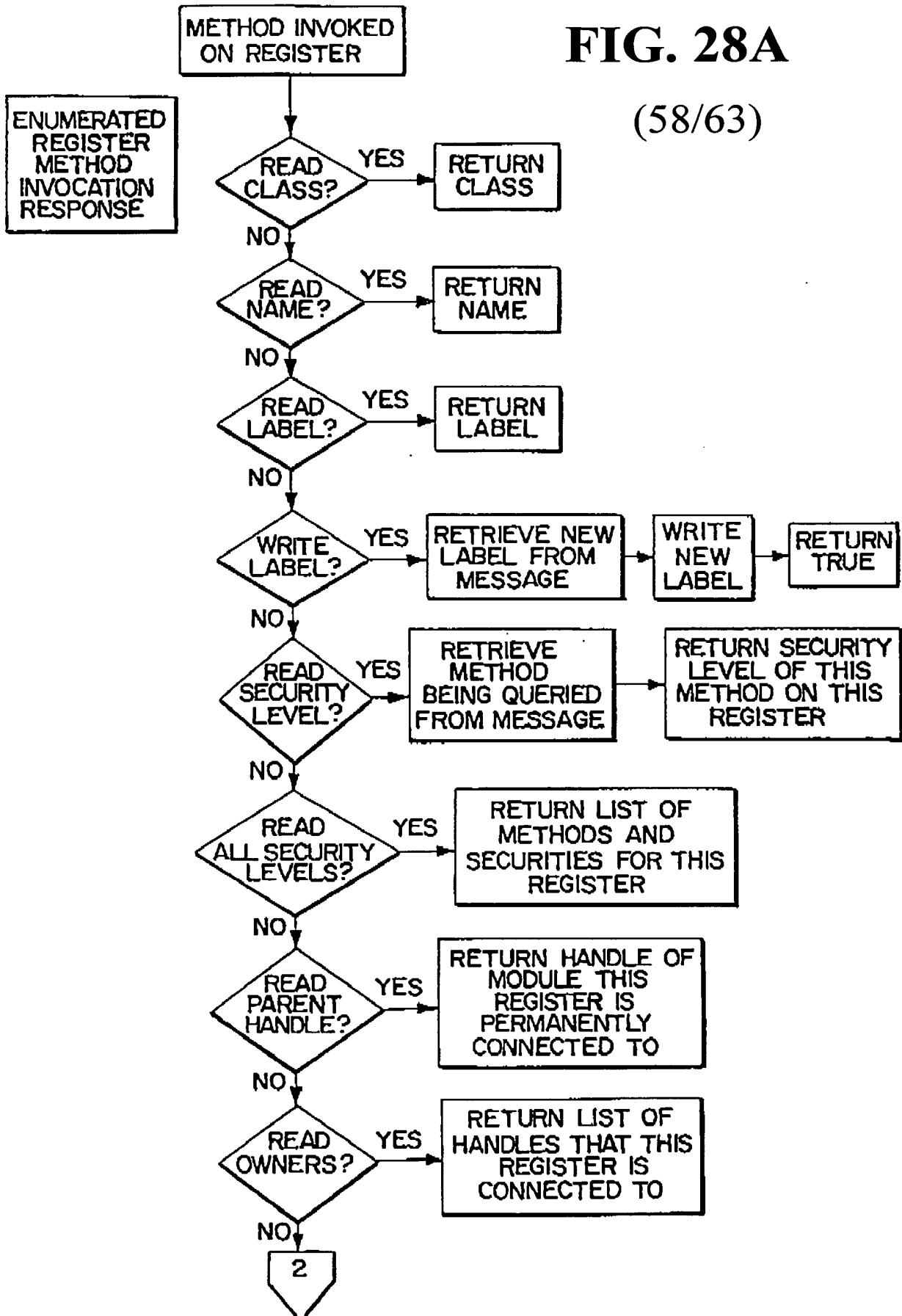


FIG. 28B

(59/63)

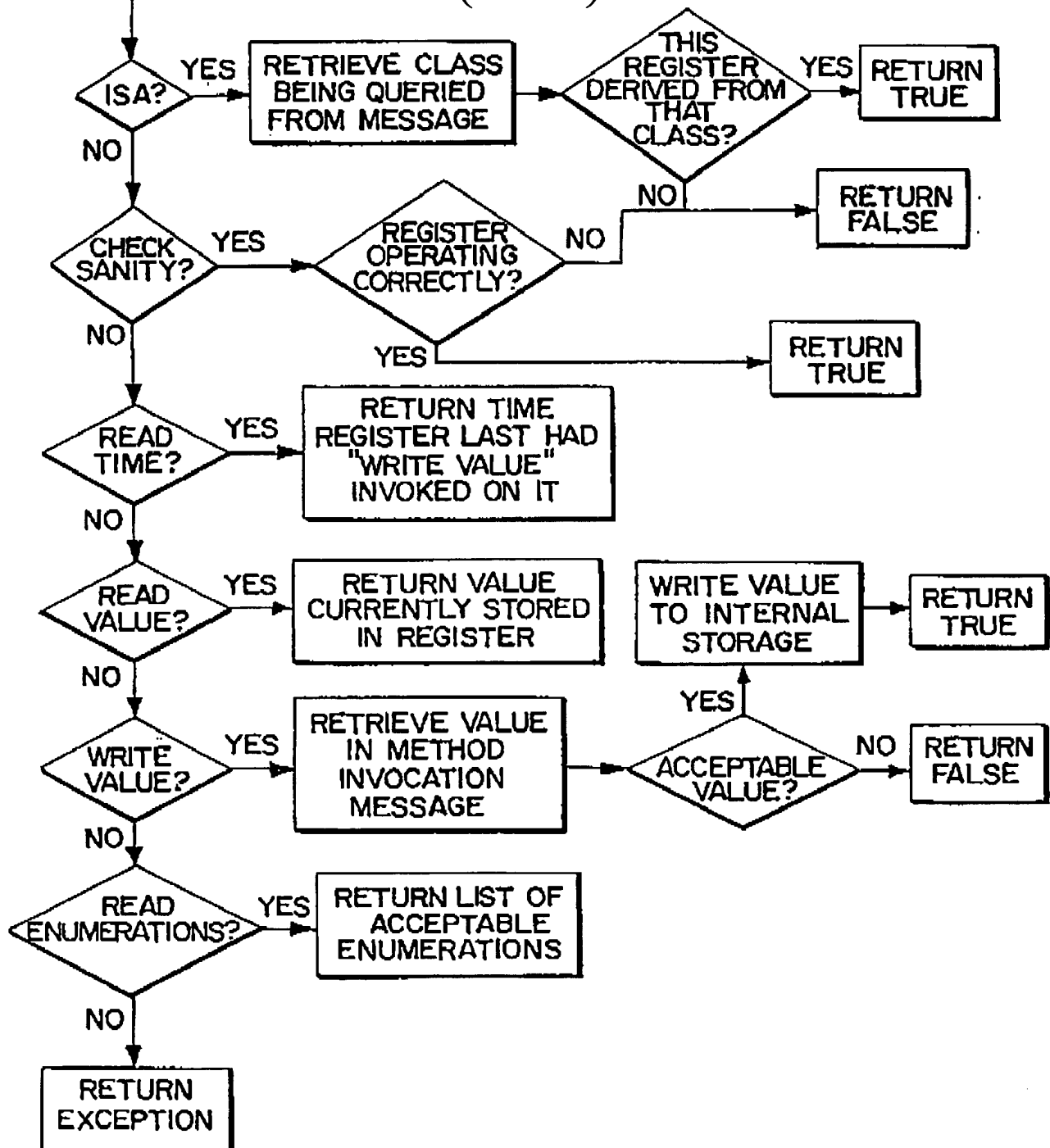


FIG. 29A

(60/63)

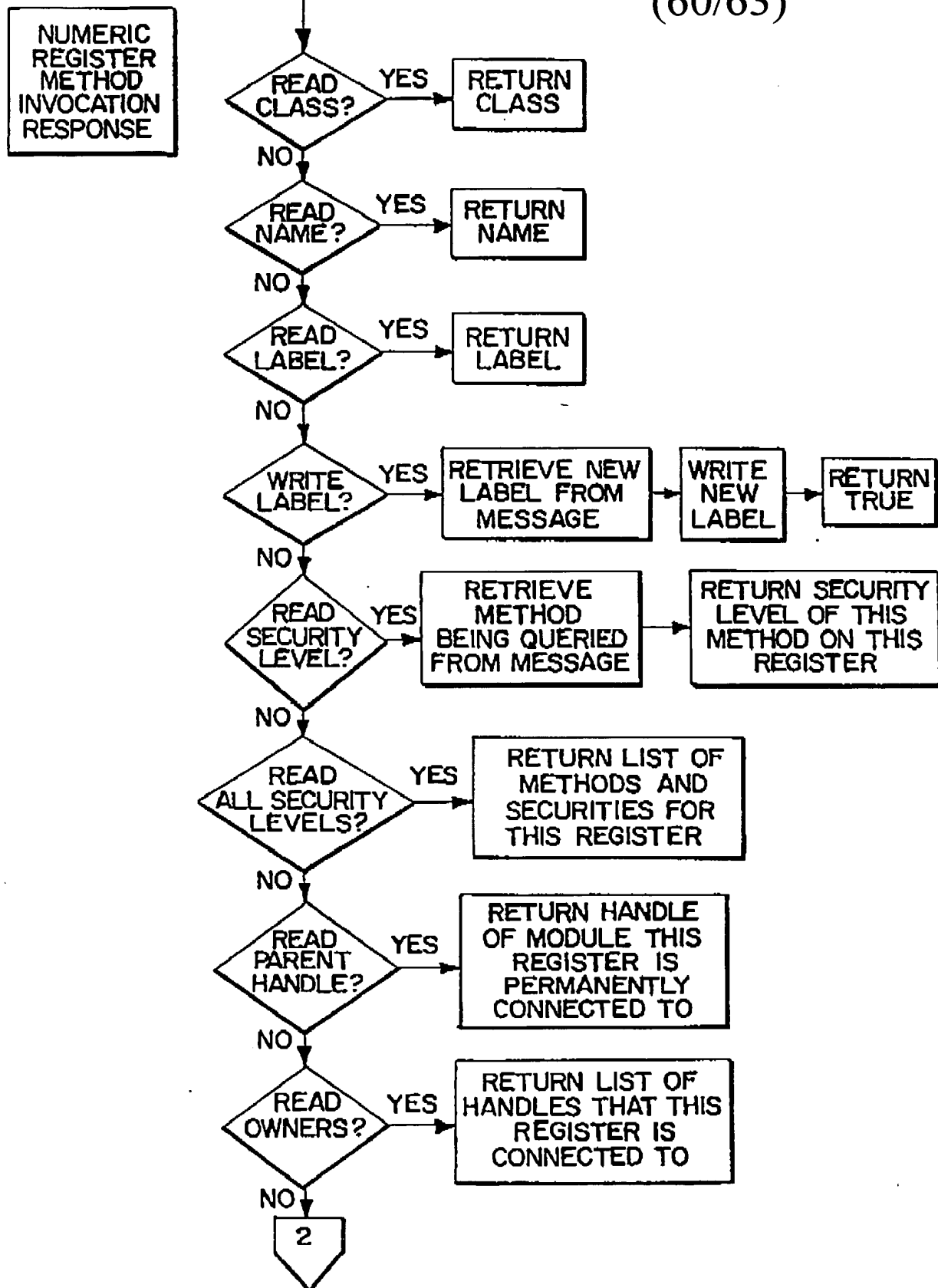


FIG. 29B

(61/63)

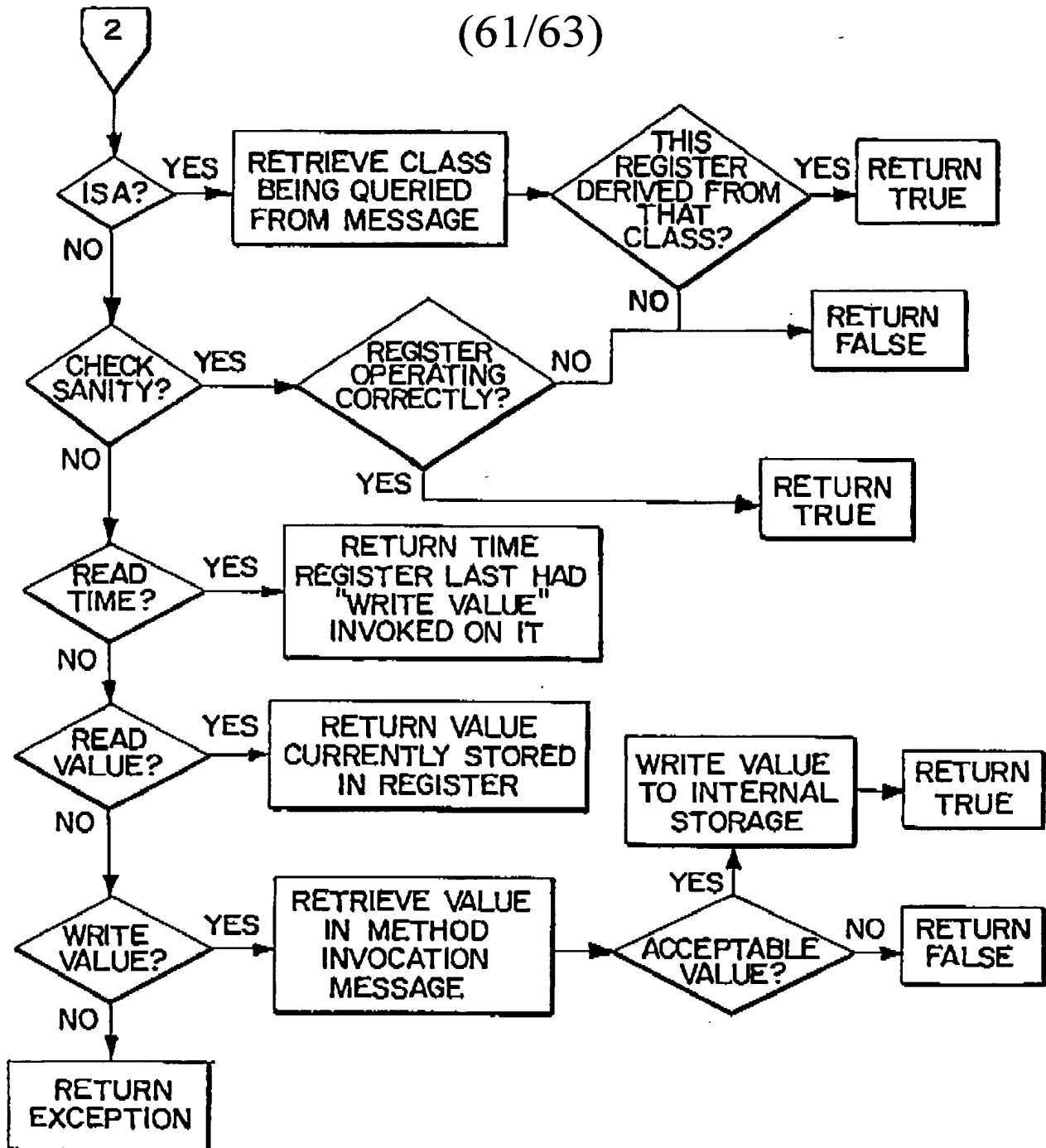
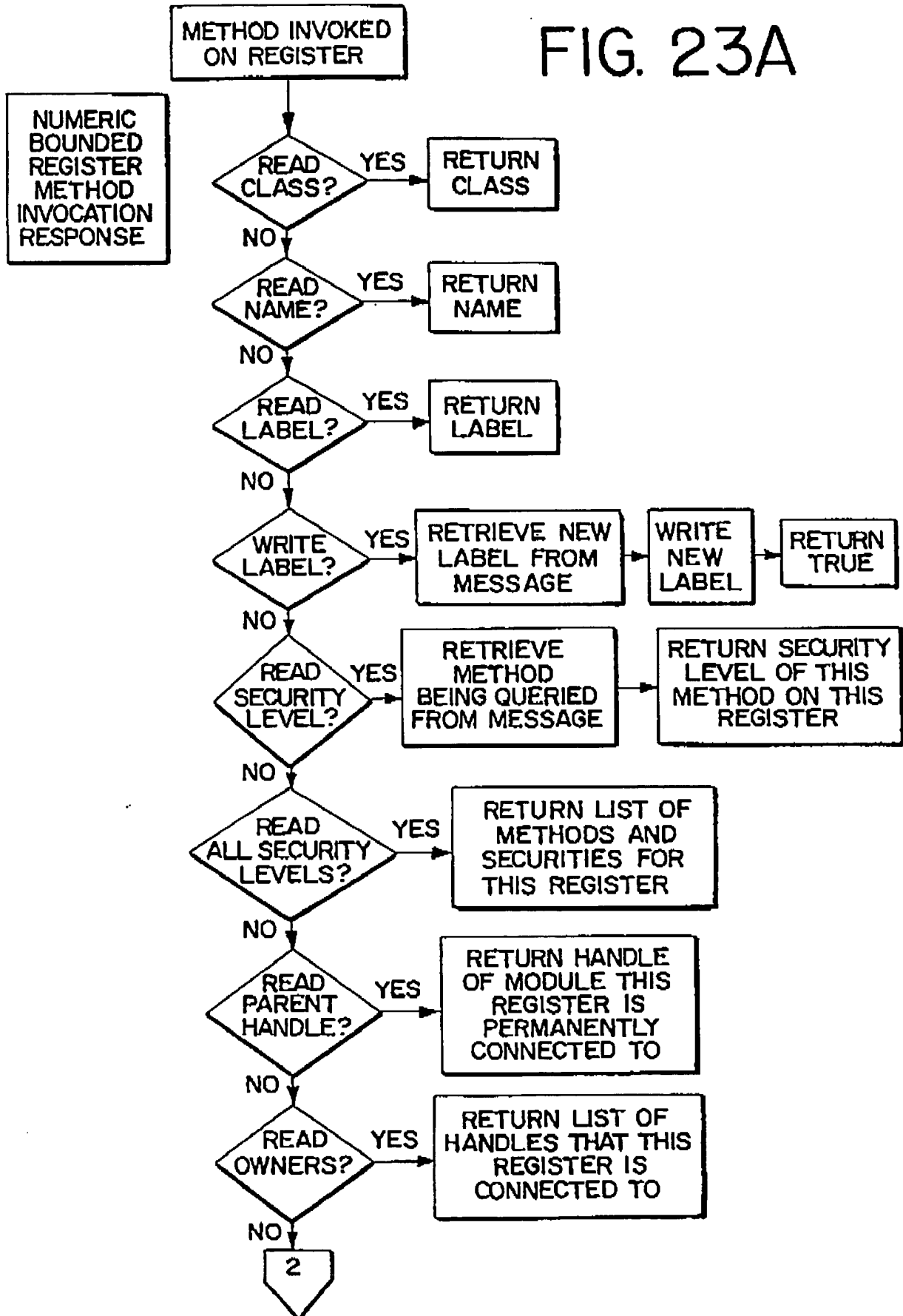


FIG. 23A



Patent Application For: PHASOR TRANSDUCER APPARATUS AND SYSTEM FOR PROTECTION, CONTROL, AND
MANAGEMENT OF ELECTRICITY DISTRIBUTION SYSTEMS

Inventor(s): Ronald G. Hart

Attorney Docket No.: 6270/108

Replacement Sheet

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FIG. 30B

(63/63)

